

产 品 规 格 书

PRODUCT SPECIFICATION

产品名称 (Model Name) <u>TFT-LCD Module</u>			
产品型号 (Part Number) <u>STI215FHD01-ELED15</u>			
项目 (Item)	拟制 (Prepared)	审核 (Checked)	批准 (Approved)
签名 (Signature)			
日期 (Date)			
备注 (Note)			
客户 (Customer):			
签名 (Signature):			
日期 (Date):			

版本介绍 (Version is introduced)				
版本 (Version)	发行日期 (Release date)	原版描述 (Old description)	更新描述 (New description)	页码 (Page)
V.0	2018/12/19	首版		/
V.1	2019/05/10	首版	open cell T215HVN05.1	/

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I Handling Precautions

- 1) Since polarizer is easily damaged, do not touch or press the surface of polarizer with hand.
- 2) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- 3) When the cell surface is soiled, wipe it with absorbent cotton or other soft cloth.
- 4) Since the cell is made of glass, it may break or crack if dropped or bumped on hard surface.
- 5) Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.
- 6) Do not press or pat the panel surface by fingers, hand or tooling.
- 7) Please handle TFT cell with care. The FPCs can only sustain for quite limited stress.
- 8) The cell package tray is packed in clean room. Please do pack & unpack it in clean room.
- 9) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT cell.
- 10) Pls avoid touching COF position while you are doing mechanical design.
- 11) When storing modules as spares for a long time, the following precaution is necessary: Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- 12) Do not apply the same pattern for a long time, it will enhance relevant defect.
- 13) If the product will be used under extreme conditions such as under high temperature, humidity, display patterns or the operation time etc., it is strongly recommended to contact AUO for the advice about the application of engineering. Otherwise, its reliability and the function may not be guaranteed. Extreme conditions are commonly found at airports, transit stations, banks, stock markets, and controlling systems.
- 14) Module designer should apply adequate thermal solutions to keep the electrical components surface temperature under control limit (ex: Source Driver IC 100°C , Components on T-con PCB 85°C)
Operations over the temperature can cause damages or decrease of lifetime.

2 General Description

This specification applies to the 21.5 inch wide Color a-Si TFT-LCD Module T215HVN05.I. The display supports the Full HD - 1920(H) x 1080(V) screen format and 16.7M colors (RGB 6-bits+Hi-FRC). The input interface is Dual channel LVDS.

2.1 Display Characteristics

The following items are characteristics summary on the table under 25°C condition:

ITEMS	Unit	SPECIFICATIONS
Screen Diagonal	[mm]	546.21 (21.50")
Active Area	[mm]	476.064 (H) x 267.786 (V)
Pixels H x V	-	1920(x3) x 1080
Pixel Pitch	[um]	247.95 (per one triad) x247.95
Pixel Arrangement	-	R.G.B. Vertical Stripe
Display Mode	-	VA Mode, Normally Black
White Luminance (Center)	[cd/m ²]	1500 (Typ.)
Contrast Ratio	-	3000 (Typ.)
Response Time	[msec]	18ms (Typ., on/off)
Power Consumption (LCD Module + Backligh unit)	[Watt]	LCD module:PDD (Typ.)=3.2W@ White pattern,Fv=60Hz Backlight unit : PBLU (Typ.)=36.0 @Is=70mA
Weight	[Grams]	TBD
Outline Dimension	[mm]	496.2(H) x 292.8(V) x 14.8(D) Typ.
Electrical Interface	-	Dual channel LVDS , RGB 6-bits + Hi-FRC data
Support Color	-	16.7M colors
Surface Treatment	-	Anti-Glare, 3H
Temperature Range		
Operating	[°C]	0 to +50
Storage (Shipping)	[°C]	-20 to +60
RoHS Compliance	-	RoHS Compliance
TCO Compliance	-	TCO 7.0 Compliance

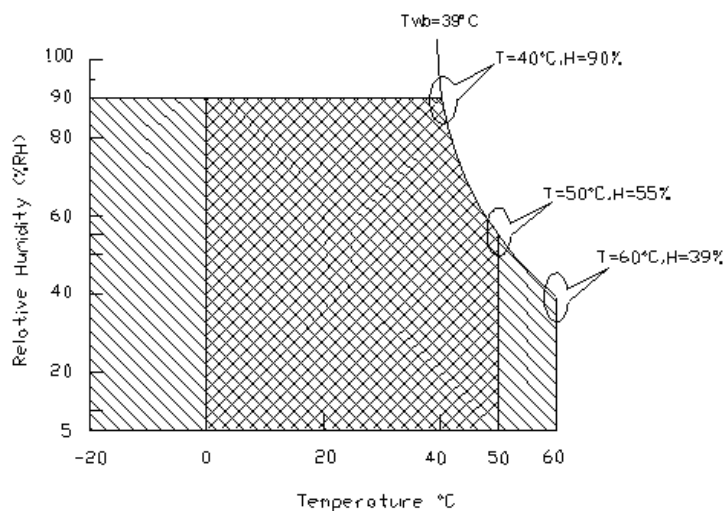
2.2 Absolute Maximum Rating of Environment

Permanent damage may occur if exceeding the following maximum rating.

Symbol	Description	Min.	Max.	Unit	Remark
TOP	Operating Temperature	0	+50	[°C]	Note 2-1
TGS	Glass surface temperature (operation)	0	+65	[°C]	Note 2-1 Function judged only
HOP	Operation Humidity	5	90	[%RH]	Note 2-1
TST	Storage Temperature	-20	+60	[°C]	
HST	Storage Humidity	5	90	[%RH]	

Note 2-1: Temperature and relative humidity range are shown as the below figure.

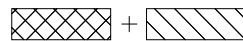
1. 90% RH Max ($T_a \leq 39^\circ\text{C}$)
2. Max wet-bulb temperature at 39°C or less. ($T_a \leq 39^\circ\text{C}$)
3. No condensation



Operating Range



Storage Range



2.3 Optical Characteristics

The optical characteristics are measured on the following test condition.

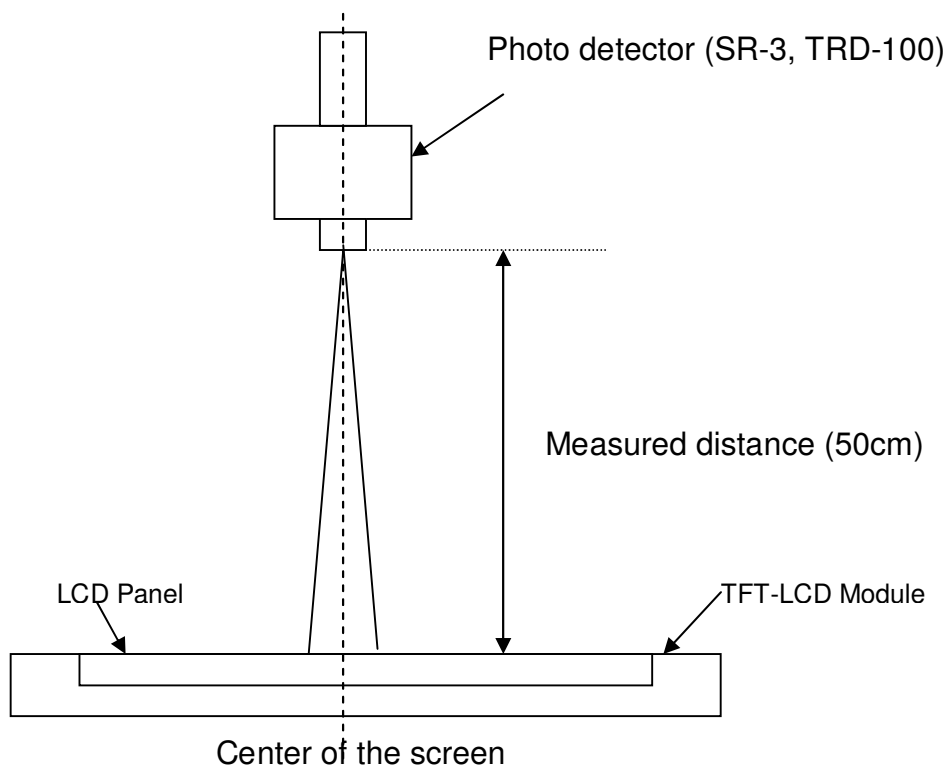
Test Condition:

1. Equipment setup: Please refer to **Note 2-2**.
2. Panel Lighting time: 30 minutes
3. VDD=5.0V, Fv=60Hz, Is=60mA, Ta=25°C

Symbol	Description		Min.	Typ.	Max.	Unit	Remark
L _w	White Luminance (Center of screen)		1400	1500	-	[cd/m2]	Note 2-2 By SR-3
L _{uni}	Luminance Uniformity (9 points)		75	80	-	[%]	Note 2-3 By SR-3
CR	Contrast Ratio (Center of screen)		2000	3000	-	-	Note 2-4 By SR-3
θ _R	Horizontal Viewing Angle (CR=10)	Right	75	89	-	[degree]	Note 2-5 By SR-3
θ _L		Left	75	89	-		
Φ _H	Vertical Viewing Angle (CR=10)	Up	75	89	-		
Φ _L		Down	75	89	-		
θ _R	Horizontal Viewing Angle (CR=5)	Right	75	89	-		
θ _L		Left	75	89	-		
Φ _H	Vertical Viewing Angle (CR=5)	Up	75	89	-		
Φ _L		Down	75	89	-		
T _R	Response Time	Rising Time	-	13	28	[msec]	Note 2-6-1 By TRD-100
T _F		Falling Time	-	5	8		
-		Rising + Falling	-	18	36		
T _{GTG}	Response Time	Gray To Gray	-	20	-	[msec]	Note 2-6-2 By TRD-100
R _x	Color Coordinates (CIE 1931)	Red x	0.627	0.657	0.687	-	By SR-3
R _y		Red y	0.305	0.335	0.365		
G _x		Green x	0.288	0.318	0.348		
G _y		Green y	0.604	0.634	0.664		
B _x		Blue x	0.124	0.154	0.184		
B _y		Blue y	0.034	0.064	0.094		
W _x		White x	0.283	0.313	0.343		
W _y		White y	0.299	0.329	0.359		
NTSC Area Ratio				72		[%]	By SR-3
CT	Crosstalk		-	-	2.0	[%]	Note 2-7 By SR-3

F _{dB}	Flicker (Center of screen)	-	-	-20	[dB]	Note 2-8 By SR-3
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Note 2-2: Equipment setup :

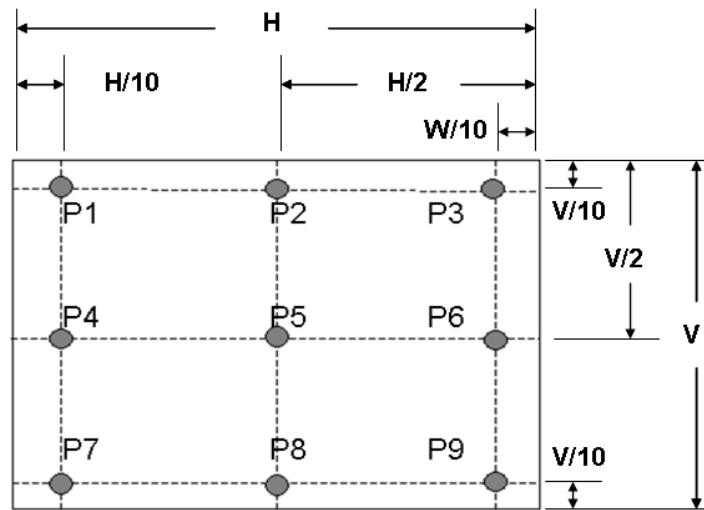


Note 2-3: Luminance Uniformity Measurement

Definition:

$$\text{Luminance Uniformity} = \frac{\text{Minimum Luminance of 9 Points (P1 ~ P9)}}{\text{Maximum Luminance of 9 Points (P1 ~ P9)}}$$

a. Test pattern: White Pattern



Note 2-4: Contrast Ratio Measurement

Definition:

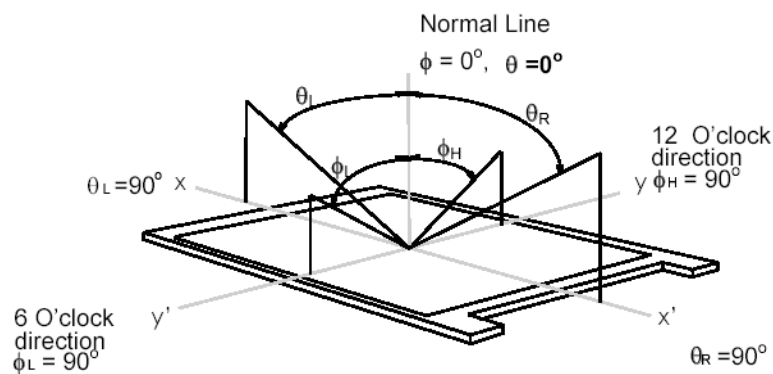
$$\text{Contrast Ratio} = \frac{\text{Luminance of White pattern}}{\text{Luminance of Black pattern}}$$

- a. Measured position: Center of screen (P5) & perpendicular to the screen ($\theta = \Phi = 0^\circ$)

Note 2-5: Viewing angle measurement

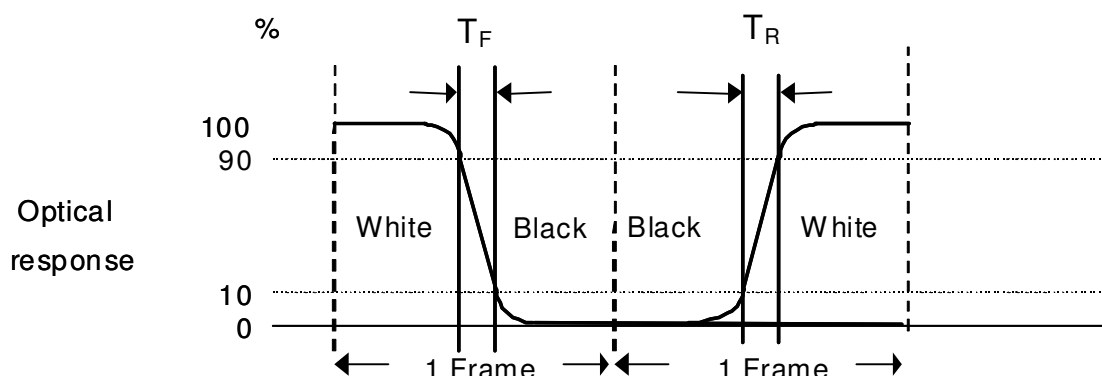
Definition: The angle at which the contrast ratio is greater than 10 & 5 .

- a. Horizontal view angle: Divide to left & right (θ_L & θ_R)
Vertical view angle: Divide to up & down (Φ_H & Φ_L)



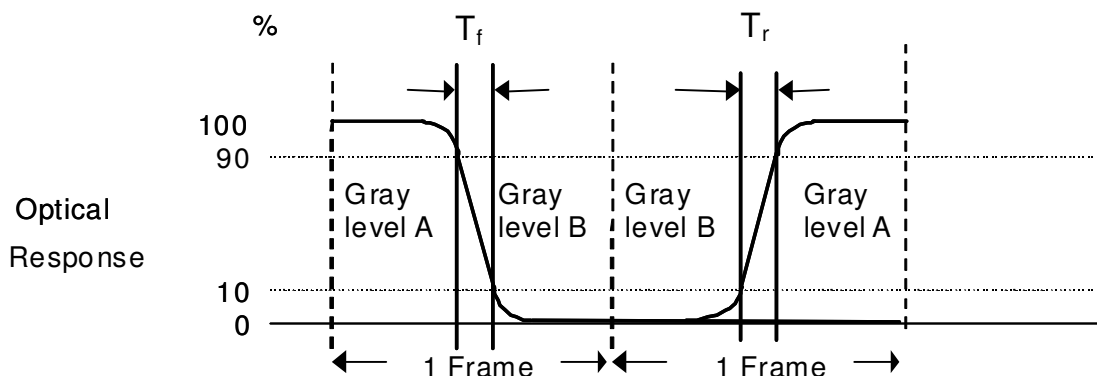
Note 2-6-1: Response time measurement

The output signals of photo detector are measured when the input signals are changed from “Black” to “White” (rising time, T_R), and from “White” to “Black” (falling time, T_F), respectively. The response time is interval between the 10% and 90% of optical response. (Black & White color definition: Please refer section 3.4.3)



Note 2-6-2: Response time measurement

The output signals of photo detector are measured when the input signals are changed from “Gray level A” to “Gray level B” (falling time, T_F), and from “Gray level B” to “Gray level A” (rising time, T_R), respectively. The response time is interval between the 10% and 90% of optical response.



The gray to gray response time is defined as the following table.

Gray Level to Gray Level		Target gray level				
		L0	L63	L127	L191	L255
Start gray level	L0					
	L63					
	L127					
	L191					
	L255					

■ T_{GTG_typ} is the total average time at rising time and falling time of gray to gray.

Note 2-7: Crosstalk measurement

Definition:

$$CT = \text{Max. } (CT_H, CT_V);$$

Where

a. Maximum Horizontal Crosstalk :

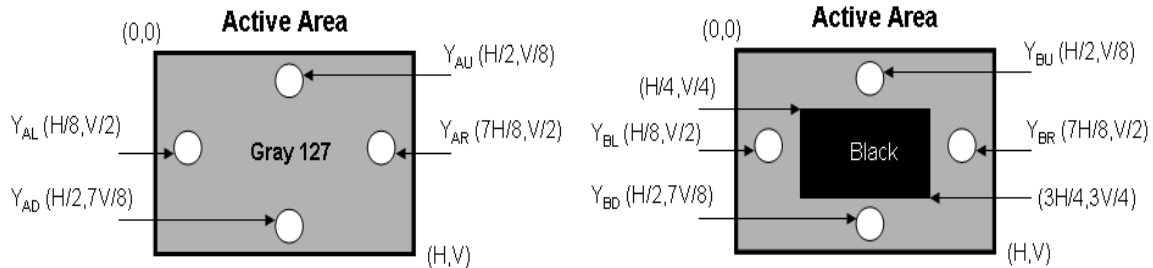
$$CT_H = \text{Max. } (|Y_{BL} - Y_{AL}| / Y_{AL} \times 100 \%, |Y_{BR} - Y_{AR}| / Y_{AR} \times 100 \%);$$

Maximum Vertical Crosstalk:

$$CT_V = \text{Max. } (|Y_{BU} - Y_{AU}| / Y_{AU} \times 100 \%, |Y_{BD} - Y_{AD}| / Y_{AD} \times 100 \%);$$

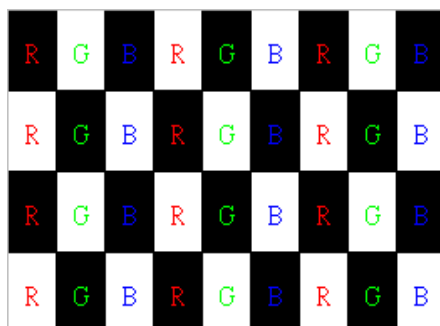
b. $Y_{AU}, Y_{AD}, Y_{AL}, Y_{AR}$ = Luminance of measured location without Black pattern

$Y_{BU}, Y_{BD}, Y_{BL}, Y_{BR}$ = Luminance of measured location with Black pattern



Note 2-8: Flicker measurement

a. Test pattern: It is listed as following.



Gray level = L0



Gray level = L127

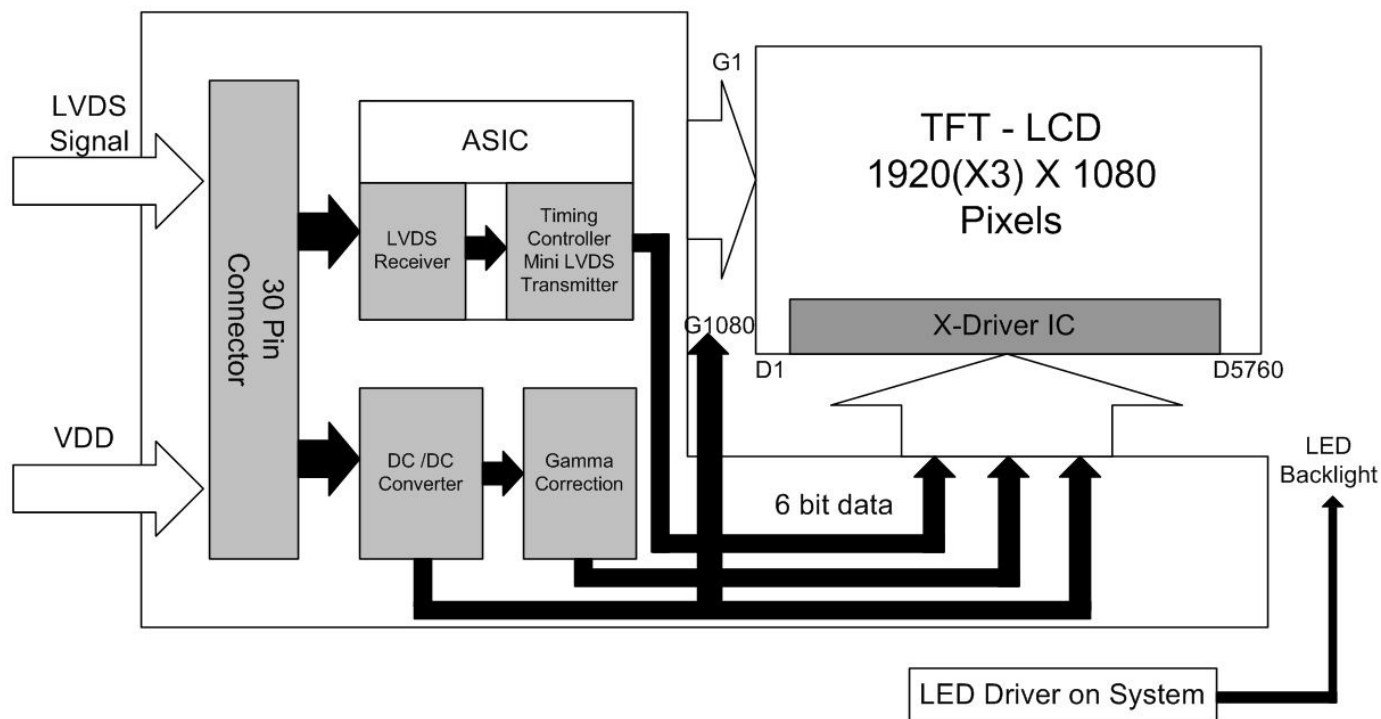
R: Red, G: Green, B:Blue

b. Measured position: Center of screen (P5) & perpendicular to the screen ($\theta=\Phi=0^\circ$)

3 TFT-LCD Module

3.1 Block Diagram

The following shows the block diagram of the 21.5 inch Color TFT-LCD Module.



3.2 Interface Connection

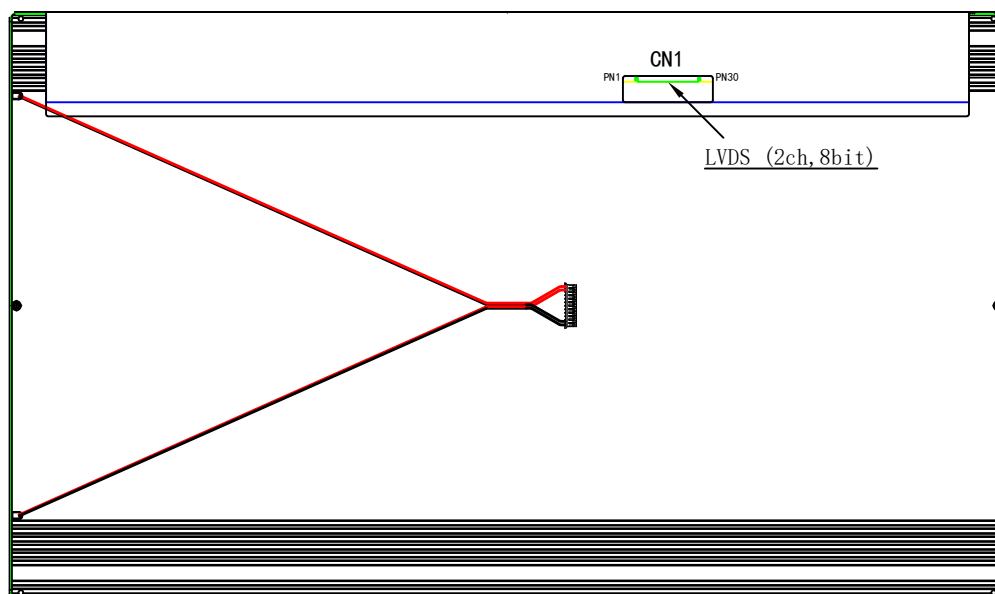
3.2.1 Connector Type

TFT-LCD Connector	Manufacturer	P-TWO	STM	FCN
	Part Number	AL230F-A0GID-P	MSCKT2407P30HB	CTI10022-3033
Mating Connector	Manufacturer	JAE or Compatible		
	Part Number	FI-X30HL (Locked Type)		

3.2.2 Connector Pin Assignment

PIN #	Symbol	Description	Remark
1	RxO0-	Negative LVDS differential data input (Odd data)	
2	RxO0+	Positive LVDS differential data input (Odd data)	
3	RxO1-	Negative LVDS differential data input (Odd data)	
4	RxO1+	Positive LVDS differential data input (Odd data)	
5	RxO2-	Negative LVDS differential data input (Odd data)	
6	RxO2+	Positive LVDS differential data input (Odd data)	
7	GND	Ground	
8	RxOCLK-	Negative LVDS differential clock input (Odd clock)	
9	RxOCLK+	Positive LVDS differential clock input (Odd clock)	
10	RxO3-	Negative LVDS differential data input (Odd data)	
11	RxO3+	Positive LVDS differential data input (Odd data)	
12	RxE0-	Negative LVDS differential data input (Even data)	
13	RxE0+	Positive LVDS differential data input (Even data)	
14	GND	Ground	
15	RxE1-	Negative LVDS differential data input (Even data)	
16	RxE1+	Positive LVDS differential data input (Even data)	
17	GND	Ground	
18	RxE2-	Negative LVDS differential data input (Even data)	
19	RxE2+	Positive LVDS differential data input (Even data)	
20	RxECLK-	Negative LVDS differential clock input (Even clock)	
21	RxECLK+	Positive LVDS differential clock input (Even clock)	
22	RxE3-	Negative LVDS differential data input (Even data)	
23	RxE3+	Positive LVDS differential data input (Even data)	
24	NC	No connection (for AUO test only. Do not connect)	
25	NC	No connection (for AUO test only. Do not connect)	
26	NC	No connection (for AUO test only. Do not connect)	

27	NC	No connection (for AUO test only. Do not connect)	
28	VDD	Power Supply Input Voltage	
29	VDD	Power Supply Input Voltage	
30	VDD	Power Supply Input Voltage	



3.3 Electrical Characteristics

3.3.1 Absolute Maximum Rating

Permanent damage may occur if exceeding the following maximum rating.

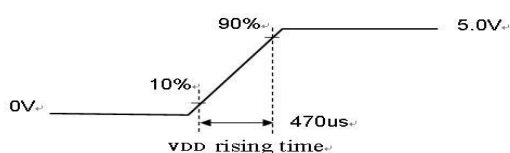
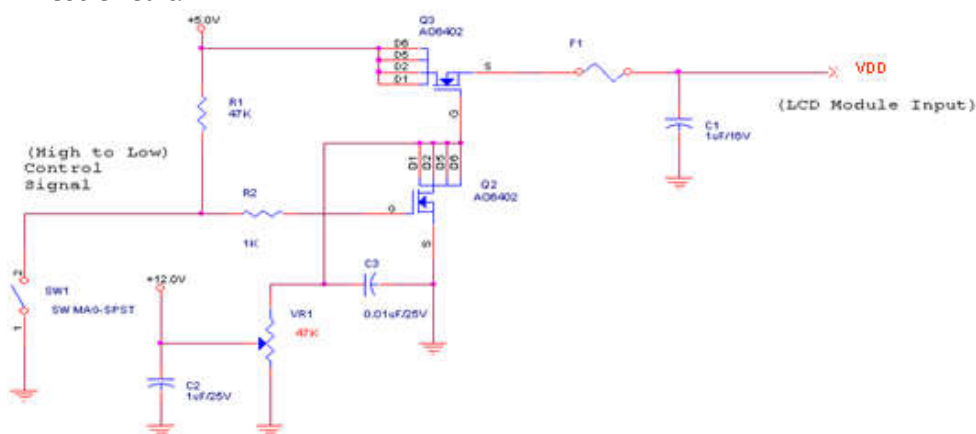
Symbol	Description	Min	Max	Unit	Remark
VDD	Power Supply Input Voltage	GND-0.3	6.0	[Volt]	Ta=25°C
SCL,SD A,WP	I2C input Voltage	GND-0.3	4.0	[Volt]	Ta=25°C

3.3.2 Recommended Operating Condition

Symbol	Description	Min	Typ	Max	Unit	Remark
VDD	Power supply Input voltage	4.5	5.0	5.5	[Volt]	
IDD	Power supply Input Current (RMS)	-	0.64	0.78	[A]	VDD= 5.0V, White Pattern, Fv=60Hz
			0.73	0.88	[A]	VDD= 5.0V, White Pattern , Fv=75Hz
PDD	VDD Power Consumption	-	3.20	3.90	[Watt]	VDD= 5.0V, White Pattern , Fv=60Hz
			3.65	4.40	[Watt]	VDD= 5.0V, White Pattern , Fv=75Hz
IRush	Inrush Current	-	-	3.0	[A]	<i>Note 3-1</i>
VDDrp	Allowable VDD Ripple Voltage	-	-	500	[mV]	VDD= 5.0V, White Pattern , Fv=75Hz

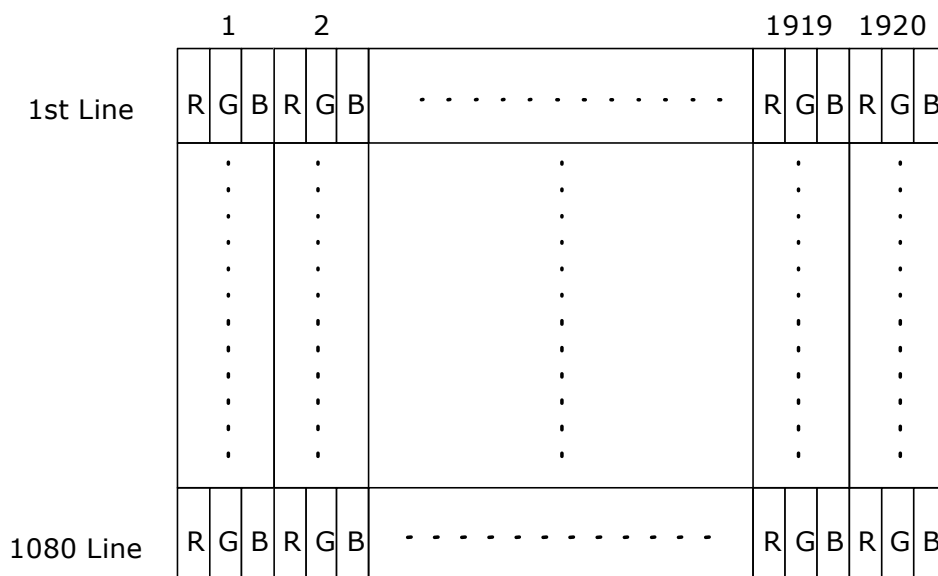
Note 3-1: Inrush Current measurement:

Test circuit:

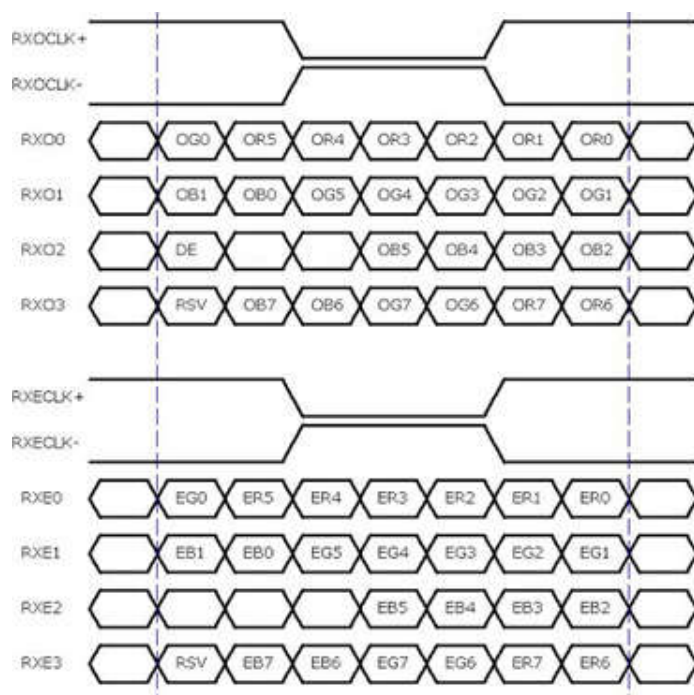


3.4 Signal Characteristics

3.4.1 LCD Pixel Format



3.4.2 LVDS Data Format



8 Bit Color Bit Order			
MSB	R7	G7	B7
	R6	G6	B6
	R5	G5	B5
	R4	G4	B4
	R3	G3	B3
	R2	G2	B2
	R1	G1	B1
LSB	R0	G0	B0

Note 3-2:

- O = "Odd Pixel Data" E = "Even Pixel Data"
- Refer to 3.4.1 LCD pixel format, the 1st data is 1 (Odd Pixel Data), the 2nd data is 2 (Even Pixel Data) and the last data is 1920 (Even Pixel Data).

3.4.3 Color versus Input Data

The following table is for color versus input data (8bit). The higher the gray level, the brighter the color.

Color	Gray Level	Color Input Data																								Remark
		RED data (MSB :R7, LSB :R0)								GREEN data (MSB :G7, LSB :G0)								BLUE data (MSB :B7, LSB :B0)								
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0	
Black	-	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
White	-	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
Gray 127	-	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	
Red	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Green	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
Blue	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	

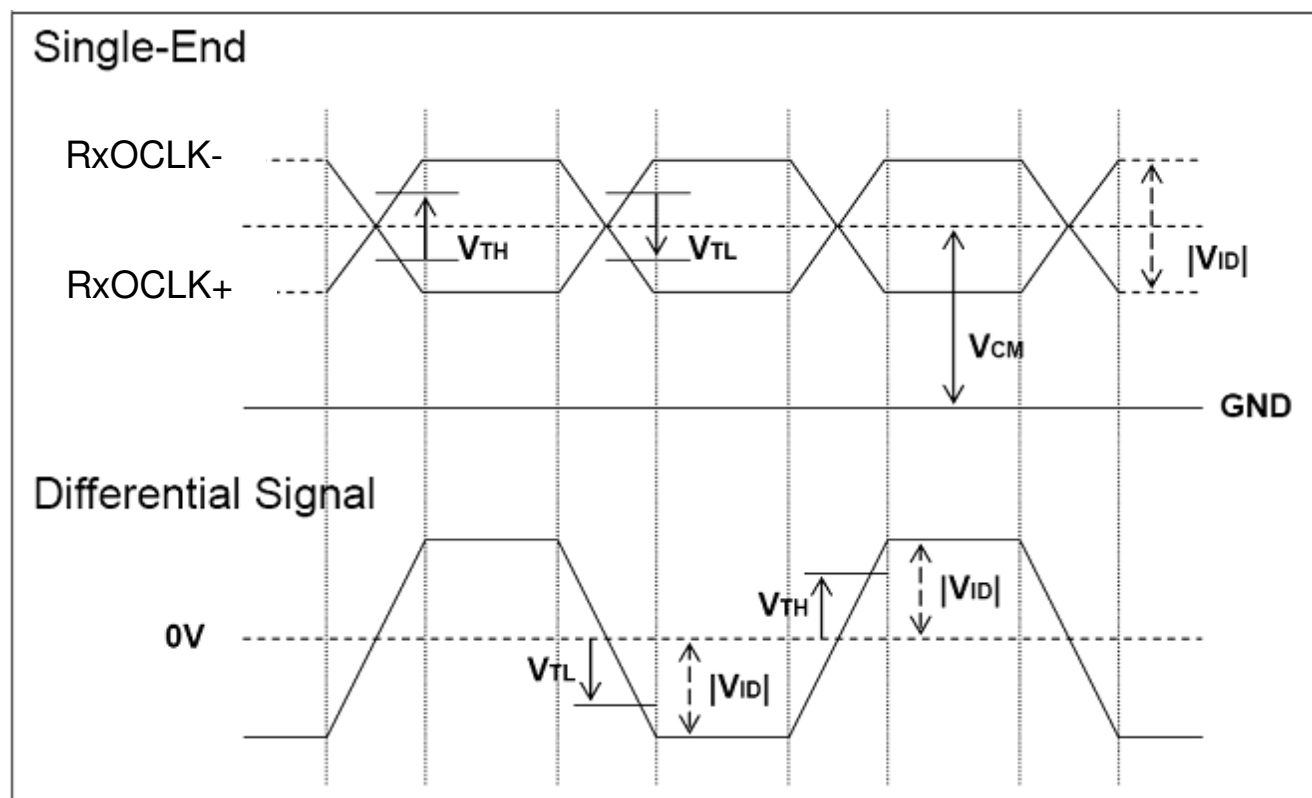
3.4.4 LVDS Specification

a. DC Characteristics:

Symbol	Description	Min	Typ	Max	Units	Condition
V_{TH}	LVDS Differential Input High Threshold	-	-	+100	[mV]	$V_{CM} = 1.2V$
V_{TL}	LVDS Differential Input Low Threshold	-100	-	-	[mV]	$V_{CM} = 1.2V$
$ V_{ID} $	LVDS Differential Input Voltage	100	-	600	[mV]	
V_{CM}	LVDS Common Mode Voltage	+1.0	+1.2	+1.5	[V]	$V_{TH}-V_{TL} = 200mV$

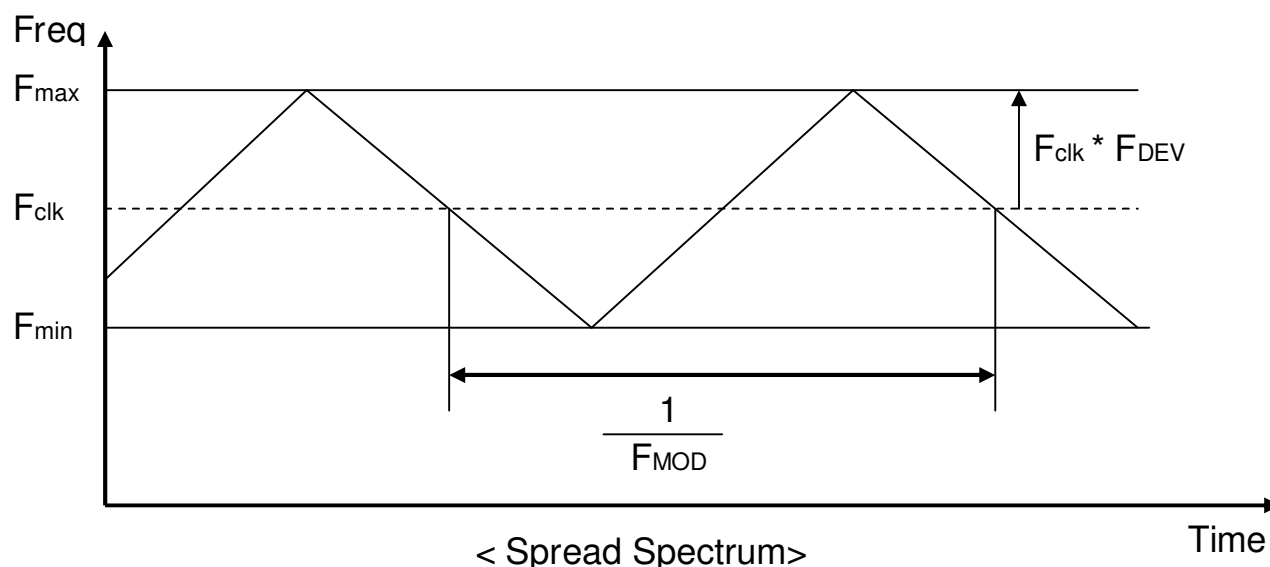
LVDS Signal Waveform:

Use RxOCLK- & RxOCLK+ as example.



b. AC Characteristics:

Symbol	Description	Min	Max	Unit	Remark
F_{DEV}	Maximum deviation of input clock frequency during Spread Spectrum	-	± 3	%	
F_{MOD}	Maximum modulation frequency of input clock during Spread Spectrum	-	200	KHz	



Fclk: LVDS Clock Frequency

3.4.5 Input Timing Specification

It only support DE mode, and the input timing are shown as the following table.

Symbol	Description		Min.	Typ.	Max.	Unit	Remark
Tv	Vertical Section	Period	1094	1130	1836	Th	
Tdisp (v)		Active	1080	1080	1080	Th	
Tblk (v)		Blanking	14	50	756	Th	
Fv		Frequency	30	60	76	Hz	
Th	Horizontal Section	Period	1000	1050	1678	Tclk	
Tdisp (h)		Active	960	960	960	Tclk	
Tblk (h)		Blanking	40	90	718	Tclk	
Fh		Frequency	53.7	67.8	90.0	KHz	Note 3-3
Tclk	LVDS Clock	Period	11.1	14.0	18.6	ns	1/Fclk
Fclk		Frequency	53.7	71.2	90.0	MHz	Note 3-4

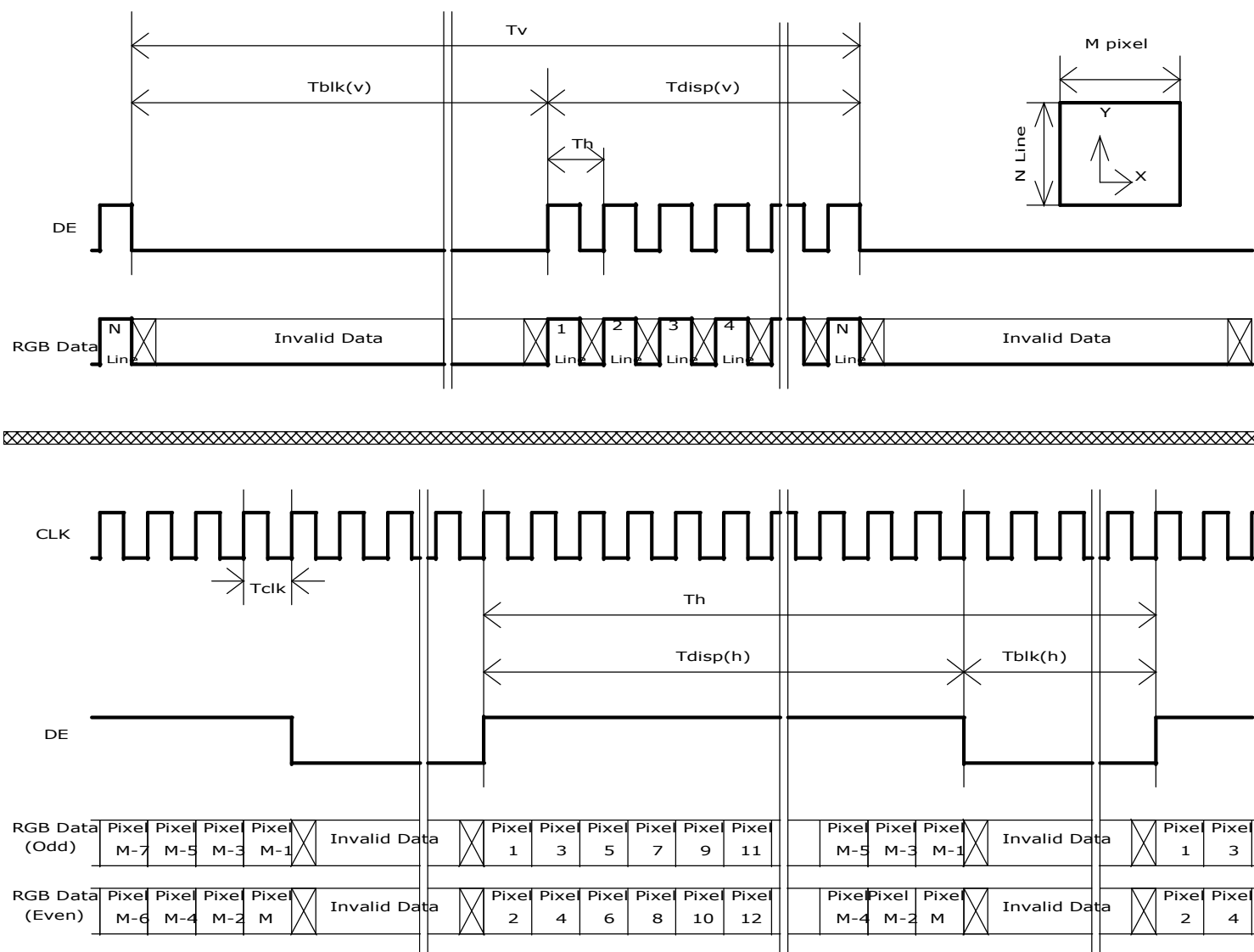
Note 3-3: The equation is listed as following. Please don't exceed the above recommended value.

$$\begin{aligned} Fh (\text{Min.}) &= Fclk (\text{Min.}) / Th (\text{Min.}); \\ Fh (\text{Typ.}) &= Fclk (\text{Typ.}) / Th (\text{Typ.}); \\ Fh (\text{Max.}) &= Fclk (\text{Max.}) / Th (\text{Min.}); \end{aligned}$$

Note 3-4: The equation is listed as following. Please don't exceed the above recommended value.

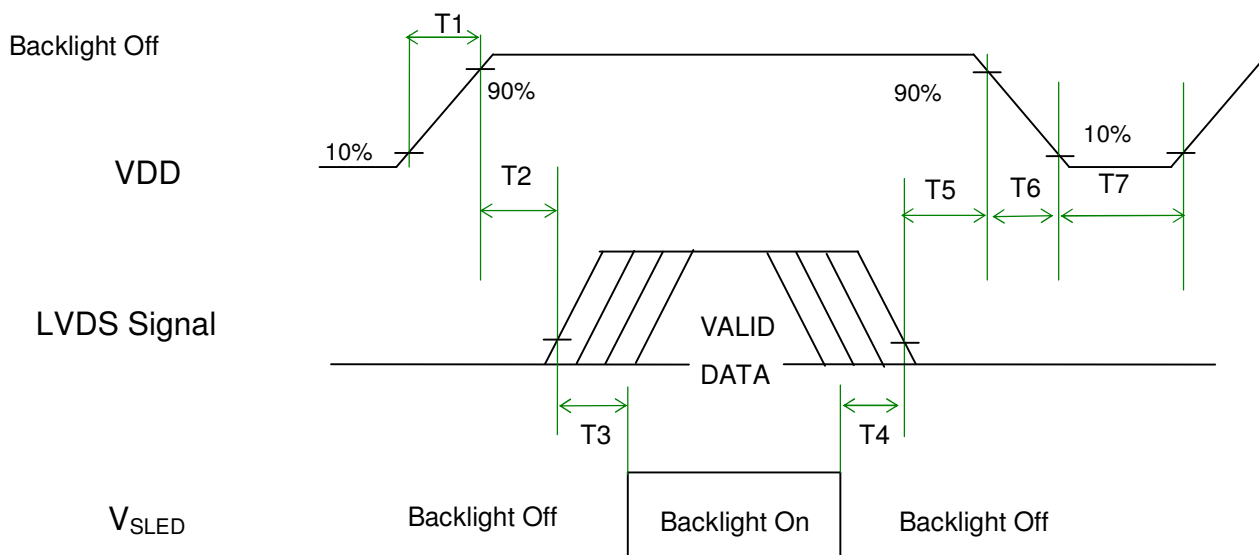
$$\begin{aligned} Fclk (\text{Min.}) &= Fv (\text{Min.}) \times Th (\text{Min.}) \times Tv (\text{Min.}); \\ Fclk (\text{Typ.}) &= Fv (\text{Typ.}) \times Th (\text{Typ.}) \times Tv (\text{Typ.}); \\ Fclk (\text{Max.}) &= Fv (\text{Max.}) \times Th (\text{Typ.}) \times Tv (\text{Typ.}); \end{aligned}$$

3.4.6 Input Timing Diagram



3.5 Power ON/OFF Sequence

VDD power, LVDS signal and backlight on/off sequence are as following. LVDS signals from any system shall be Hi-Z state when VDD is off.



Power Sequence Timing

Symbol	Value			Unit	Remark
	Min.	Typ.	Max.		
T1	0.5	-	10	[ms]	
T2	0	-	50	[ms]	
T3	500	-	-	[ms]	
T4	100	-	-	[ms]	
T5	0		50	[ms]	Note 3-5 Note 3-6
T6	0	-	200	[ms]	Note 3-7
T7	1000	-	-	[ms]	

Note 3-5 : Recommend setting T5 = 0ms to avoid electronic noise when VDD is off.

Note 3-6 : During T5 and T6 period , please keep the level of input LVDS signals with Hi-Z state.

Note 3-7 : Voltage of VDD must decay smoothly after power-off. (customer system decide this value)

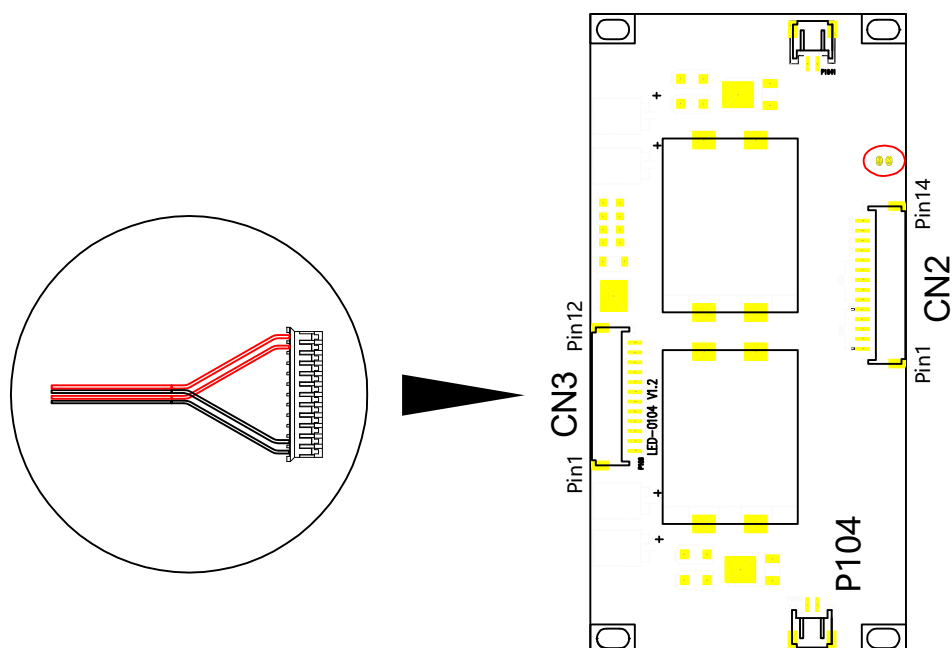
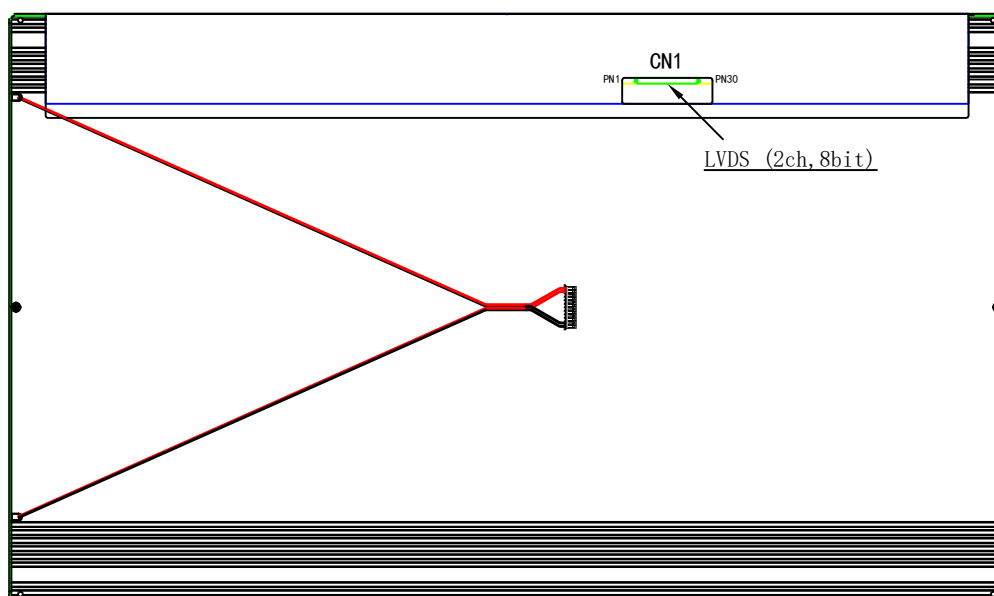
4 Backlight Unit

4.1 Interface Connection

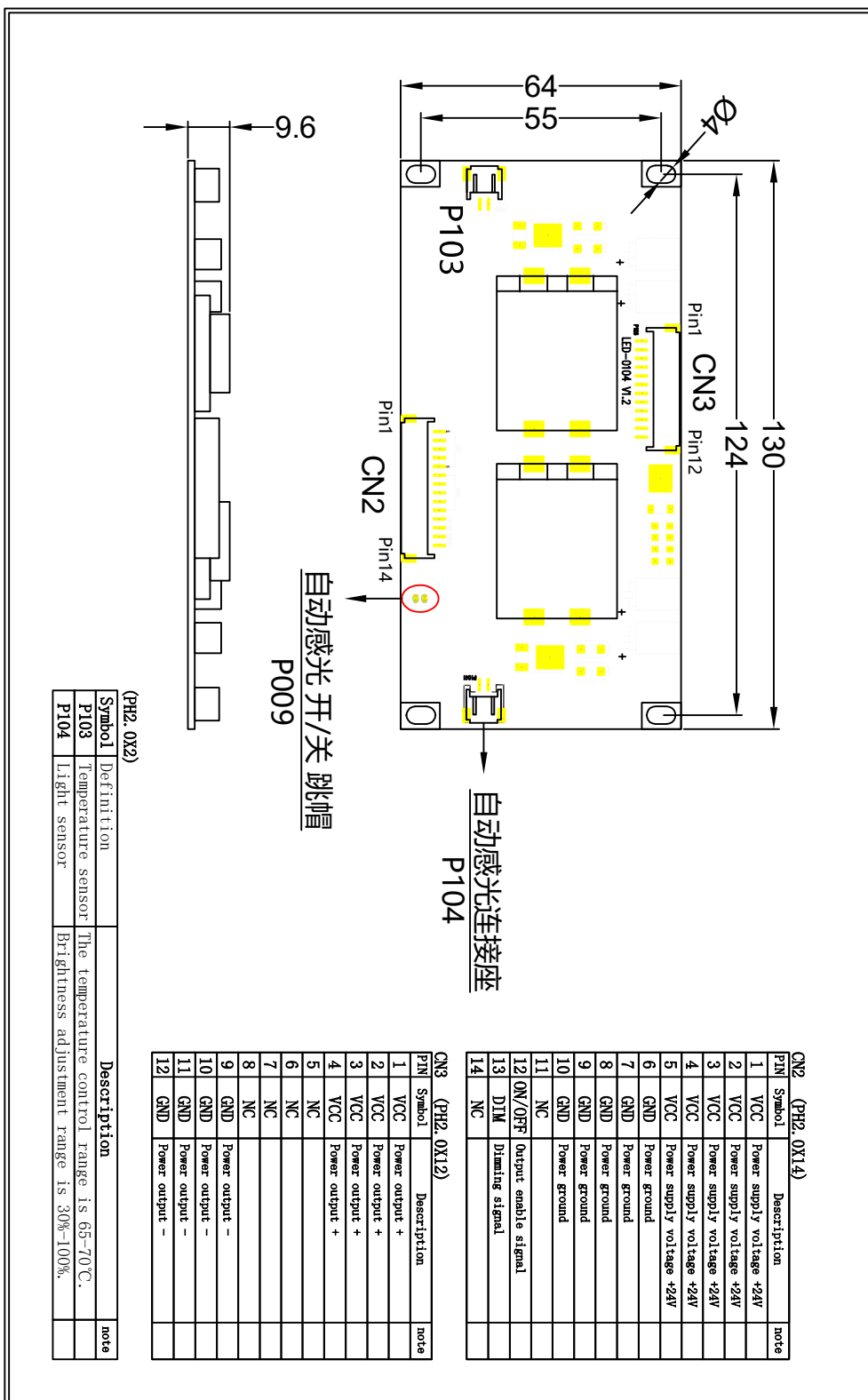
4.1.1 Connector Type

Backlight Connector	LED lights line	26# 2Pin X2
	LED line terminal	PH2.0X12
Mating Connector	Manufactur	
	Part Number	

Backlight Connector dimension:



4.1.2 Connector Pin Assignment



4.2 Electrical Characteristics

4.2.1 Absolute Maximum Rating

LED electrical characteristics

(Ta=25°C)

Symbol	Description	Min.	Typ.	Max.	Unit	Remark
Is	LED String Current	-	--	--	[mA]	100% duty ratio of LED chip
Vs	LED String Voltage	-	--	--	[Volt]	Is=--mA @ 100% duty ratio;

4.2.2 Recommended Operating Condition

Constant current plate electrical characteristics

Ta = 25 ± 2 °C

Parameter			Symbol	Values			Unit	notes
				Min	Typ	Max		
LED Driver :								
Power Supply Input Voltage			VBL	12.0	24.0	26.4	Vdc	1
Power Supply Input Current			IBL	3.0	1.50		A	1
Power Supply Input Current (In-Rush)			In-rush	-	-	(TBD)	A	VBL = 24.0V ExtV _{BR-B} = 100% 3
Power Consumption			PBL	-	36.0		W	1
Input Voltage for Control System Signals	On/Off	On	V on	2.5	-	5.5	Vdc	
		Off	V off	-0.3	0.0	0.5	Vdc	
	Brightness Adjust		ExtV _{BR-B}	30	-	100	%	On Duty 5
				30	-	100	%	
	ExtV _{BR-B} Frequency		f _{PWM}	500	-	1500	Hz	
	Pulse Duty Level (PWM)		High Level	2.5	-	5.5	Vdc	HIGH : on duty LOW : off duty
			Low Level	0.0	-	0.5	Vdc	
LED :								
Life Time				30,000	50,000		Hrs	2

notes :

- Electrical characteristics are determined after the unit has been 'ON' and stable for approximately 60 minutes at 25±2°C. The specified current and power consumption are under the typical supply Input voltage 24V and VBR (ExtV_{BR-B} : 100%), it is total power consumption.
- The life time (MTTF) is determined as the time which luminance of the LED is 50% compared to that of initial value at the typical LED current (ExtV_{BR-B} : 100%) on condition of continuous operating in LCM state at 25±2°C.
- The duration of rush current is about 200ms. This duration is applied to LED on time.
- Even though inrush current is over the specified value, there is no problem if I²T spec of fuse is satisfied. ExtV_{BR-B} signal have to input available duty range and sequence.
- After Driver ON signal is applied, ExtV_{BR-B} should be sustained from 30% to 100% more than 500ms. After that, ExtV_{BR-B} 30% and 100% is possible

5 Reliability Test

AUO reliability test items are listed as following table. (*Bare Panel only*)

Items	Condition	Remark
Temperature Humidity Bias (THB)	Ta= 50°C , 80%RH, 300hours	
High Temperature Operation (HTO)	Ta= 50°C , 50%RH, 300hours	
Low Temperature Operation (LTO)	Ta= 0°C , 300hours	
High Temperature Storage (HTS)	Ta= 60°C , 300hours	
Low Temperature Storage (LTS)	Ta= -20°C , 300hours	
Vibration Test (Non-operation)	Acceleration: 1.5 Grms Wave: Random Frequency: 10 - 200 Hz Sweep: 30 Minutes each Axis (X, Y, Z)	
Shock Test (Non-operation)	Acceleration: 50 G Wave: Half-sine Active Time: 20 ms Direction: ±X, ±Y, ±Z (one time for each Axis)	
Thermal Shock Test (TST)	-20°C/30min, 60°C/30min, 100 cycles	Note 5-1
On/Off Test	On/10sec, Off/10sec, 30,000 cycles	
ESD (Electro Static Discharge)	Contact Discharge: ± 15KV, 150pF(330Ω) 1sec, 8 points, 25 times/ point.	Note 5-2
	Air Discharge: ± 15KV, 150pF(330Ω) 1sec 8 points, 25 times/ point.	
Altitude Test	Operation:18,000 ft Non-Operation:40,000 ft	

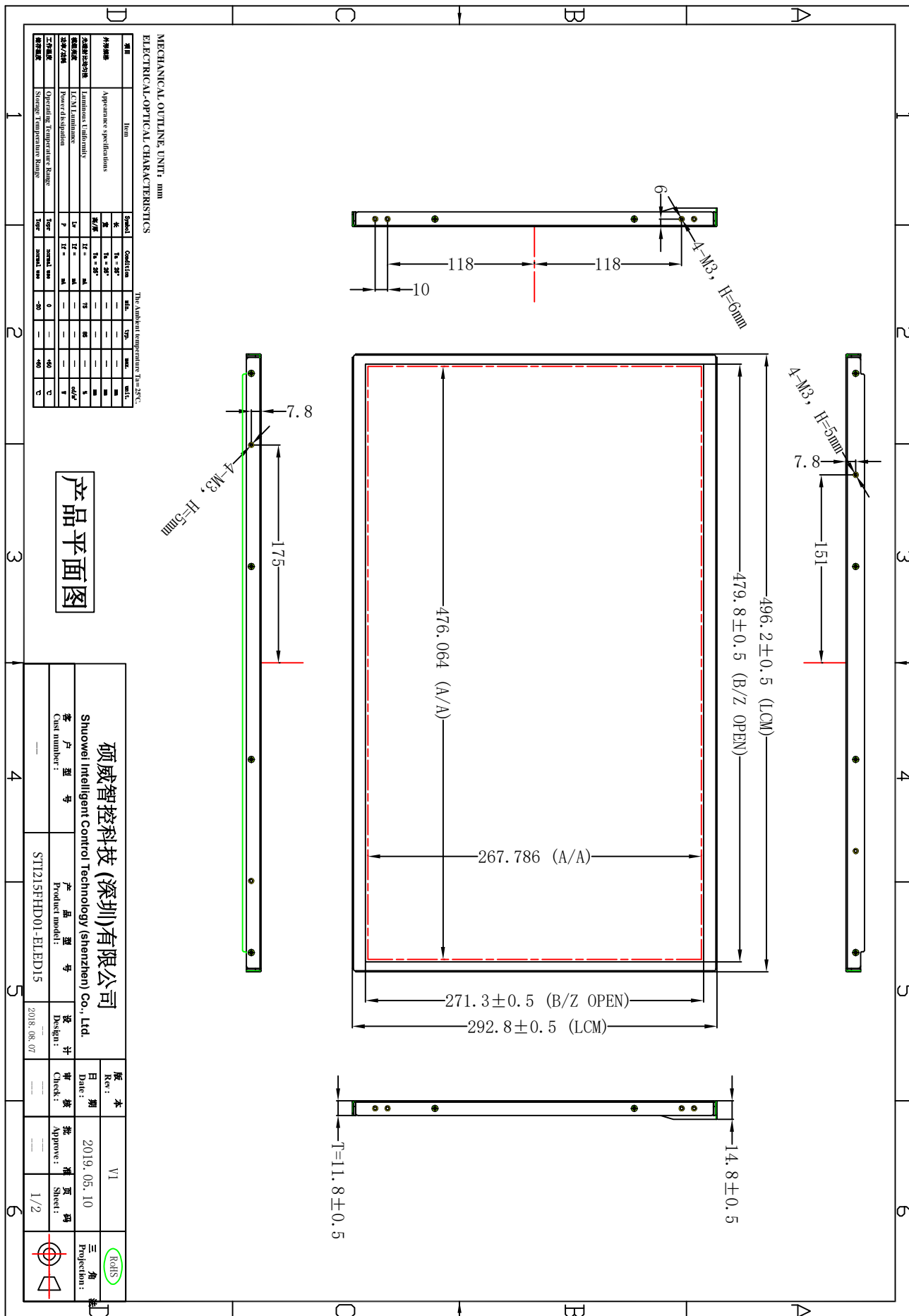
Note 5-1: a. A cycle of rapid temperature change consists of varying the temperature from -20°C to 60°C, and back again. Power is not applied during the test.
b. After finish temperature cycling, the unit is placed in normal room ambient for at least 4 hours before power on.

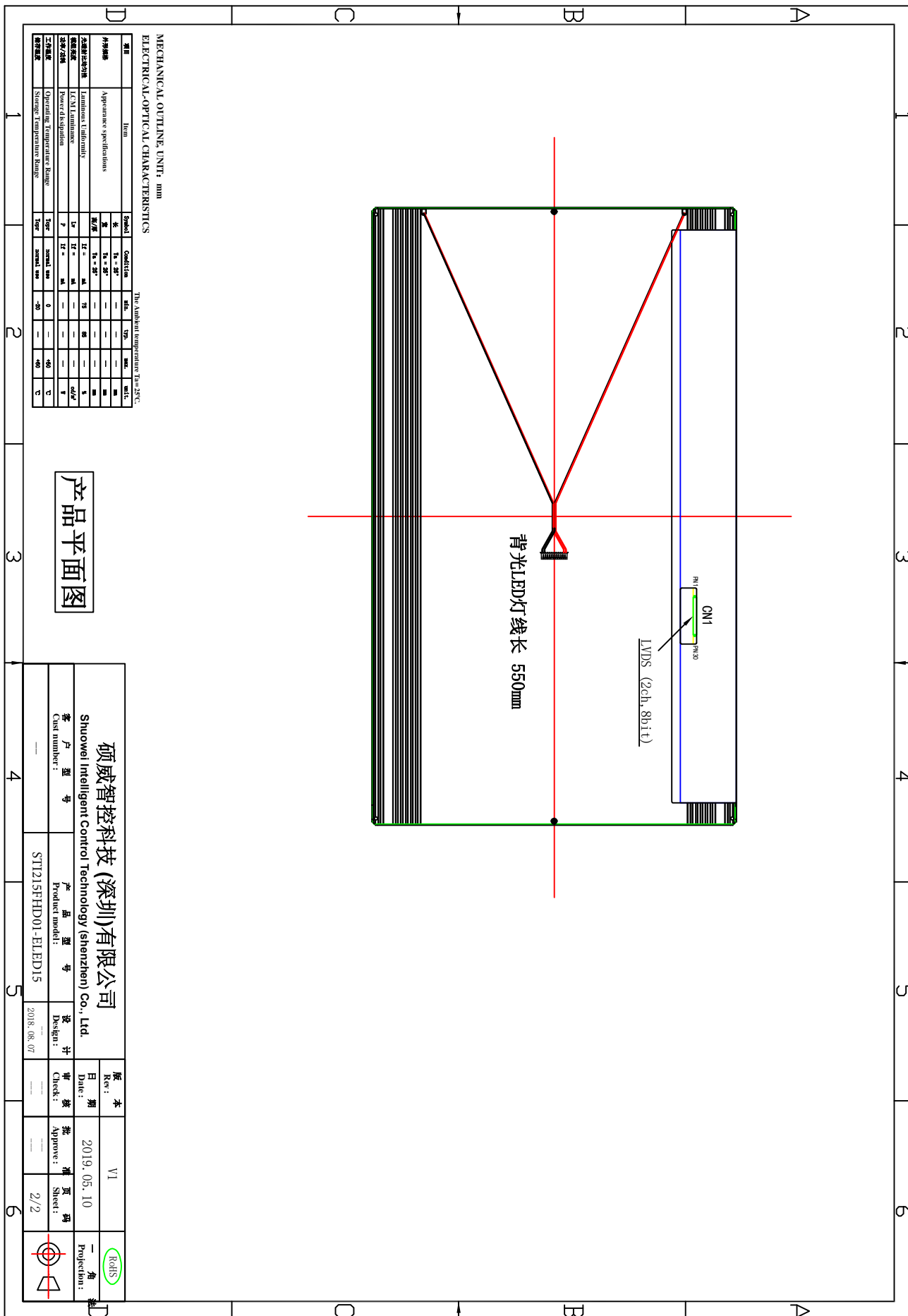
Note 5-2: EN61000-4-2, ESD class B: Certain performance degradation allowed
No data lost
Self-recoverable
No hardware failures.

6 Shipping Label

TBD

7 Mechanical Characteristics





8 Packing Specification

8.1 Packing Flow

8.2 Pallet and shipment information

TBD