产 品 规 格 书

PRODUCT SPECIFICATION

产品名称 (Model Name)	TFT-LCD Module
产品型号 (Part Number)	STI27FHD01-ELED15

项目（Ltem）	拟制（Prepared）	审核（Checked）	批准（Approved）
签名（Signature）			
日期（Date）			
备注（Note）			

客户（Customer）：

签名（Signature）：

日期（Date）：

版本介绍 (Version is introduced)

版本 (Version)	发行日期 (Release date)	原版描述 (Old description)	更新描述 (New description)	页码 (Page)
V.0	2017/09/20	首版		/
V.1	2018/02/27		外形尺寸缩小	
V.2	2018/12/21		open cell 更换为 AUO面板	

Contents

I Handling Precautions

1) Since polarizer is easily damaged, do not touch or press the surface of polorizer with hand.
2) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
3) When the cell surface is soiled, wipe it with absorbent cotton or other soft cloth.
4) Since the cell is made of glass, it may break or crack if dropped or bumped on hard surface.
5) Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.
6) Do not press or pat the panel surface by fingers, hand or tooling.
7) Please handle TFT cell with care. The FPCs can only sustain for quite limited stress.
8) The cell package tray is packed in clean room. Please do pack & unpack it in clean room.
9) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT cell.
10) Pls avoid touching COF position while you are doing mechanical design.
11) When storing modules as spares for a long time, the following precaution is necessary: Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5℃ and 35℃ at normal humidity.
12) Do not apply the same pattern for a long time, it will enhance relevant defect.

2 General Description

This specification applies to the 27 inch wide Color a-Si TFT-LCD Module STI27FHD01-15 The display supports the FHD - 1920(H) x 1080(V) screen format and 16.7M colors (8-bits RGB data input). The input interface is Dual channel LVDS.

2.1 Display Characteristics

The following items are characteristics summary on the table under 25℃ condition:

<Table 1. General Specifications>

Items	Specification	Unit	Note
Active Screen Size	27”	inch	
Number of Pixels	1920 X 1080	Pixel	
Display Area	596.736 (H) x 335.664 (V)	mm	
Pixel Pitch	0.3108 (H) x 0.3108 (V)	mm	
Bezel Opening	600.0(H) x 338.4(V)	mm	
Outline Dimension	628.0(H) x 364.4(V) x 18.5(D)	mm	
Display Colors	8-bit, 16.7 M colors	Colors	
Display Operation Mode	VA Mode Normally Black		
Pixel Arrangement	RGB vertical stripe		
Luminance, White	1500 (Center 1 point,Typ.)	cd/m^2	
Power Consumption	Total = 67.2 (Typ.)	W	LCD Open Cell = 7.2 W(Typ.)@W_{hite}pattern,Fv=60Hz Backlight = 60 W(Typ.)
Surface Treatment	Anti-Glare		
Viewing Angle (CR>10)	+89/-89(H), +89/-89(V) Typ.		(CR≥10)

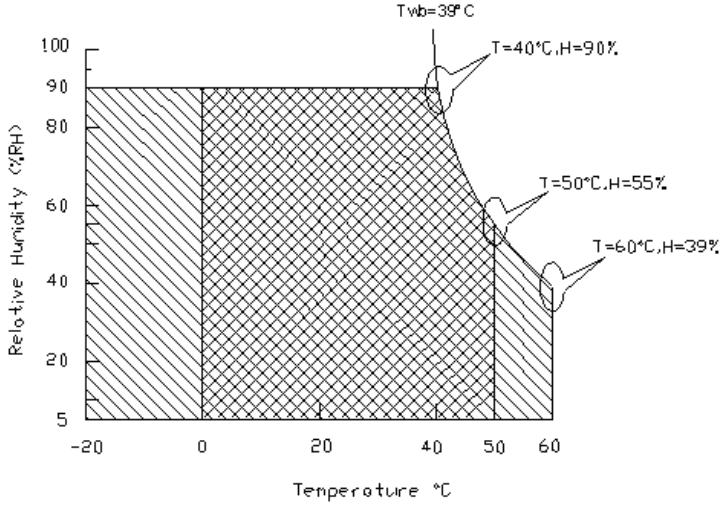
2.2 Absolute Maximum Rating of Environment

Permanent damage may occur if exceeding the following maximum rating.

Symbol	Description	Min.	Max.	Unit	Remark
TOP	Operating Temperature	0	+50	[°C]	***Note 2-1***
TGS	Glass surface temperature (operation)	0	+65	[°C]	***Note 2-1*** *Function judged only*
HOP	Operation Humidity	5	90	[%RH]	***Note 2-1***
TST	Storage Temperature	-20	+60	[°C]	
HST	Storage Humidity	5	90	[%RH]	

Note 2-1: Temperature and relative humidity range are shown as the below figure.

1. 90% RH Max (Ta ≦39℃)
2. Max wet-bulb temperature at 39℃ or less. (Ta ≦39℃)
3. No condensation



Operating Range Storage Range +

2.3 Optical Characteristics

Optical characteristics are determined after the unit has been 'ON' and stable in a dark environment at 25±2°C. The values are specified at 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0 °.
FIG. 1 shows additional information concerning the measurement equipment and method.

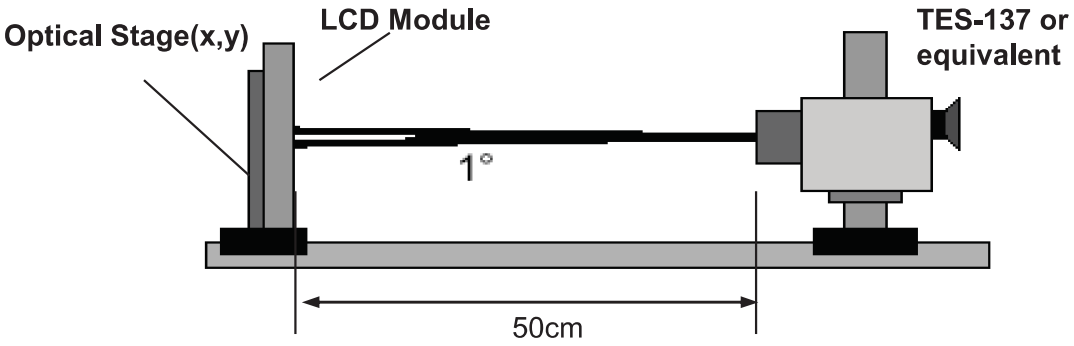


FIG. 1 Optical Characteristic Measurement Equipment and Method

Ta= 25±2°C, V_{LCD}=12.0V, fv=60Hz, Dclk=74.25MHz,
EXTVBR-B =100% Back Light : Yz B/L

OPTICAL CHARACTERISTICS

Parameter		Symbol	Values			Unit	Notes
			Min.	Typ.	Max		
Contrast Ratio		CR	2400	3000	--		1,2
Surface Luminance (White)		L_{WH} (2D)	1350	1500	--	cd/m^2	1,3
		L_{WH} (3D)			-		
Luminance Variation		$\delta_{WHITE(9P)}$	--	--	-		1,4
Response Time (G to G)		Tγ	--	14	--	ms	5
Color Gamut		NTSC		80		%	1,6
Color Coordinates							6
	Red	R_X	Typ.-0.03	0.659	Typ.+0.03		
		R_Y		0.325			
	Green	G_X		0.270			
		G_Y		0.585			
	Blue	B_X		0.136			
		B_Y		0.109			
	White	W_X					
		W_Y					
Viewing Angle							5
2D	x axis, right(φ=0°)	θ_r	--	89	--	degree	
	x axis, left(φ=180°)	θ_l	--	89	--	degree	
	y axis, up(φ=90°)	θ_u	--	89	--	degree	
	y axis, down (φ=270°)	θ_d	--	89	--	degree	
3D	y axis, up	θ_u				degree	
	y axis, down	θ_d				degree	
3D cross talk (middle)		--					

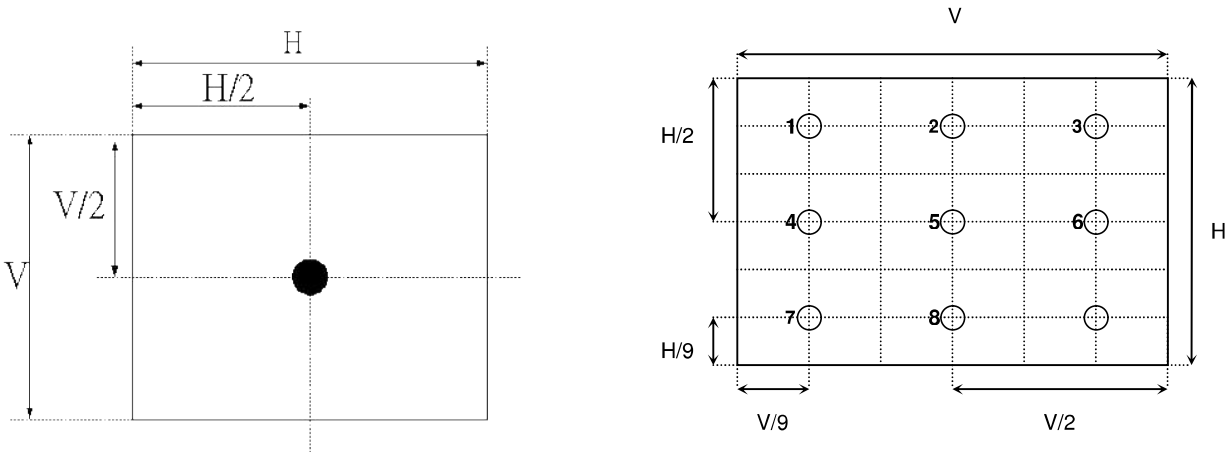
Note:

1. Contrast Ratio (CR) is defined mathematically as:

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance of } L_{on5}}{\text{Surface Luminance of } L_{off5}}$$

2. Surface luminance is luminance value at point 5 across the LCD surface 50cm from the surface with all pixels displaying white. From more information see FIG 2. When LED input VDDB =24V, I_{DDB}. = 3.00, L_{WH}=Lon5 where Lon5 is the luminance with all pixels displaying white at center 5 location.

FIG. 2 Luminance



3. The variation in surface luminance, δWHITE is defined (center of Screen) as:

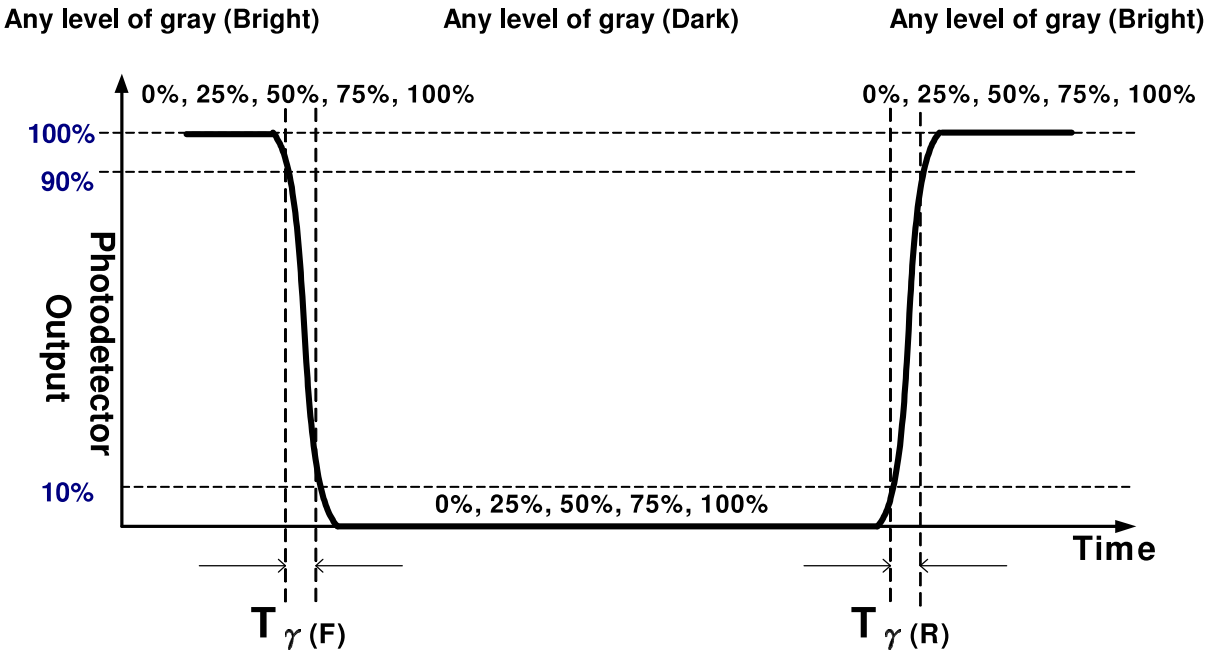
$\delta_{WHITE(9P)}$= Maximum(L_{on1}, L_{on2},…,L_{on9})/ Minimum(L_{on1}, L_{on2},…L_{on9})

4. Response time T_γ is the average time required for display transition by switching the input signal for five luminance ratio (0%,25%,50%,75%,100% brightness matrix) and is based on F_v=60Hz to optimize.

Measured Response Time		Target				
		0%	25%	50%	75%	100%
Start	0%		0% to 25%	0% to 50%	0% to 75%	0% to 100%
	25%	25% to 0%		25% to 50%	25% to 75%	25% to 100%
	50%	50% to 0%	50% to 25%		50% to 75%	50% to 100%
	75%	75% to 0%	75% to 25%	75% to 50%		75% to 100%
	100%	100% to 0%	100% to 25%	100% to 50%	100% to 75%	

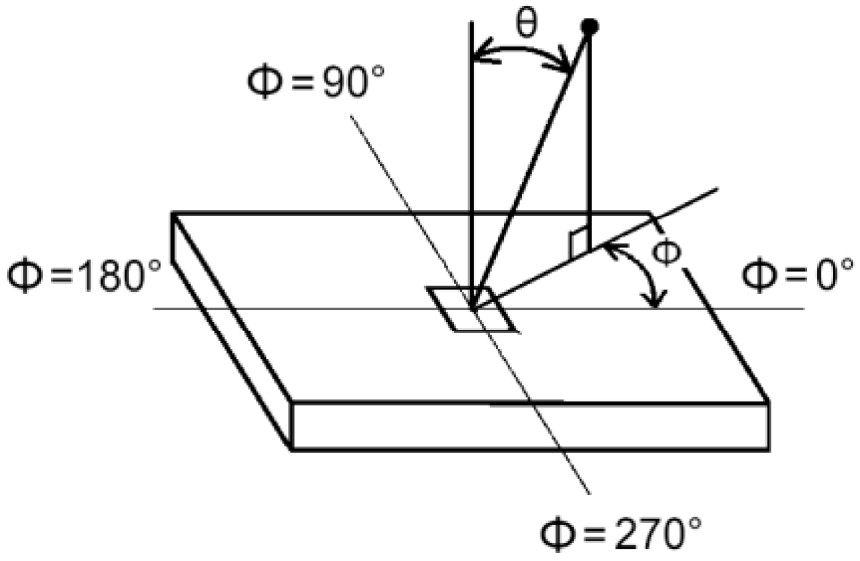
T_γ is determined by 10% to 90% brightness difference of rising or falling period. (As illustrated)

The response time is defined as the following figure and shall be measured by switching the input signal for "any level of grey(bright) " and "any level of gray(dark)".



5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG3.

FIG.3 Viewing Angle



6. Definition of Transmittance (T%):

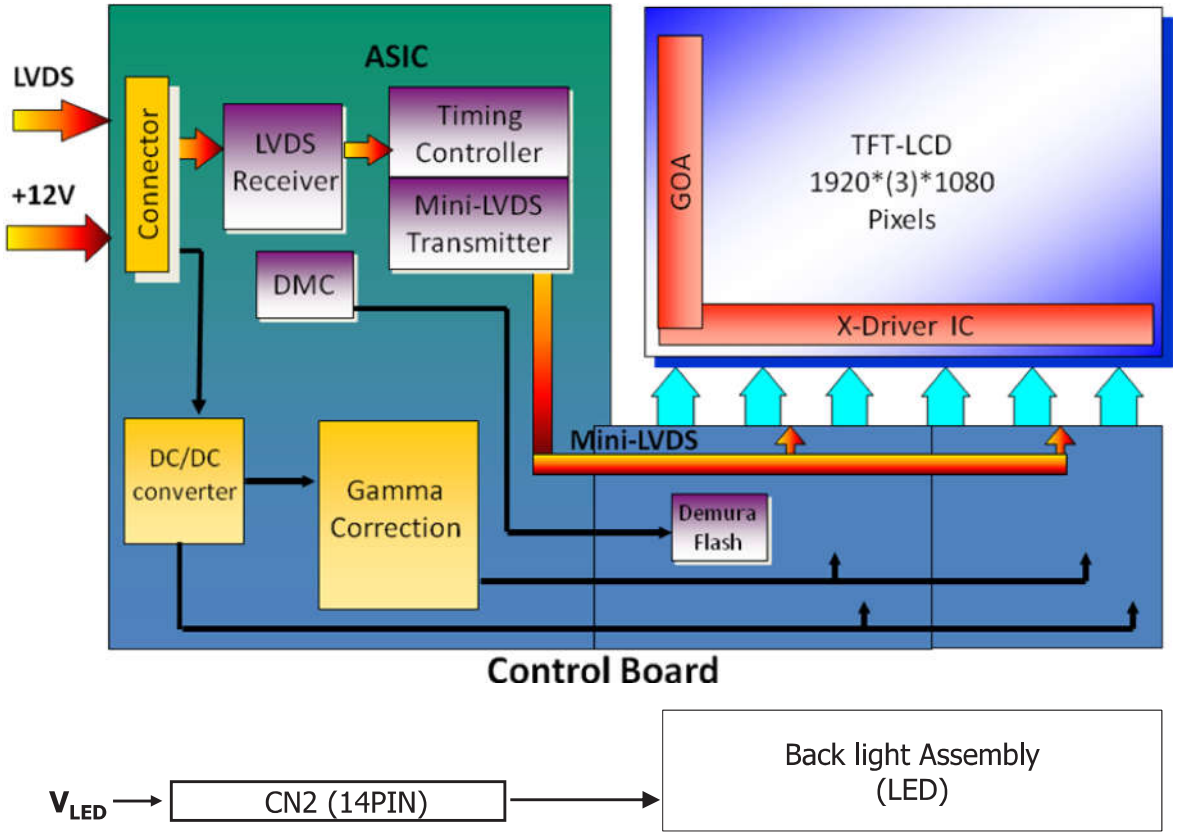
$$\text{Transmittance} = \frac{\text{Luminance of LCD module}}{\text{Luminance of backlight}} * 100\%$$

During transmittance measurement, the backlight of LCD module contains no brightness enhancement film. Two diffuser sheets which diffuse the light source uniformly are suggested to use for transmittance measurement.

3 TFT-LCD Module

3.1 Block Diagram

The following shows the block diagram of the 27 inch Color TFT-LCD Module.



3.2 Interface Connection

3.2.1 Connector Type

TFT-LCD Connector	Manufacturer	STARCOON	STM	P-TWO
	Part Number	093G30-00001A-M4	MSAKT2407P30_HB	187178-30091-3
Mating Connector	Manufacturer	JAE or Compatible		
	Part Number	FI-X30HL (Locked Type)		

3.2.2 Connector Pin Assignment

PIN #	Symbol	Description	Remark
1	RxO0-	Negative LVDS differential data input (Odd data)	
2	RxO0+	Positive LVDS differential data input (Odd data)	
3	RxO1-	Negative LVDS differential data input (Odd data)	
4	RxO1+	Positive LVDS differential data input (Odd data)	
5	RxO2-	Negative LVDS differential data input (Odd data)	
6	RxO2+	Positive LVDS differential data input (Odd data)	
7	GND	Ground	
8	RxOCLK-	Negative LVDS differential clock input (Odd clock)	
9	RxOCLK+	Positive LVDS differential clock input (Odd clock)	
10	RxO3-	Negative LVDS differential data input (Odd data)	
11	RxO3+	Positive LVDS differential data input (Odd data)	
12	RxE0-	Negative LVDS differential data input (Even data)	
13	RxE0+	Positive LVDS differential data input (Even data)	
14	GND	Ground	
15	RxE1-	Negative LVDS differential data input (Even data)	
16	RxE1+	Positive LVDS differential data input (Even data)	
17	GND	Ground	
18	RxE2-	Negative LVDS differential data input (Even data)	
19	RxE2+	Positive LVDS differential data input (Even data)	
20	RxECLK-	Negative LVDS differential clock input (Even clock)	
21	RxECLK+	Positive LVDS differential clock input (Even clock)	
22	RxE3-	Negative LVDS differential data input (Even data)	
23	RxE3+	Positive LVDS differential data input (Even data)	
24	WP	Digital-Vcom write protection	
25	NC	No connection (for AUO test only. Do not connect)	
26	SCL	I2C-Compatible Serial-Clock Input for Vcom adjust	
27	SDA	I2C-Compatible Serial-Data Input / Output for Vcom adjust	
28	VDD	Power Supply Input Voltage	
29	VDD	Power Supply Input Voltage	
30	VDD	Power Supply Input Voltage	

3.2.3 Backlight Interface Connection

CN2:Input terminal PH2.0-6(2.0mmX14)

PIN #	Symbol	Description
1	V_{DDB}	Operating Voltage Supply, +24V DC Regulated
2	V_{DDB}	Operating Voltage Supply, +24V DC Regulated
3	V_{DDB}	Operating Voltage Supply, +24V DC Regulated
4	V_{DDB}	Operating Voltage Supply, +24V DC Regulated
5	V_{DDB}	Operating Voltage Supply, +24V DC Regulated
6	GND	Ground
7	GND	Ground
8	GND	Ground
9	GND	Ground
10	GND	Ground
11		Not connect
12	VBLON	BL On-Off: High (2.5~5.5V) for BL , Low/Open (0~0.5V) for BL **off**
13	VDIM (note 1)	**Internal PWM Dimming** High (5.5V/100% Duty) for 100% Lum; <NC; when external PWM>
14		Not connect

Note (1) PWM dimming function is included internal PWM and external PWM. Internal PWM: input voltage 0 (GND) ~5.5V to pin 13th, and duty ratio of output voltage/current of inverter is from 30% to 100%.

3.3 Electrical Characteristics

3.3.1 Absolute Maximum Rating

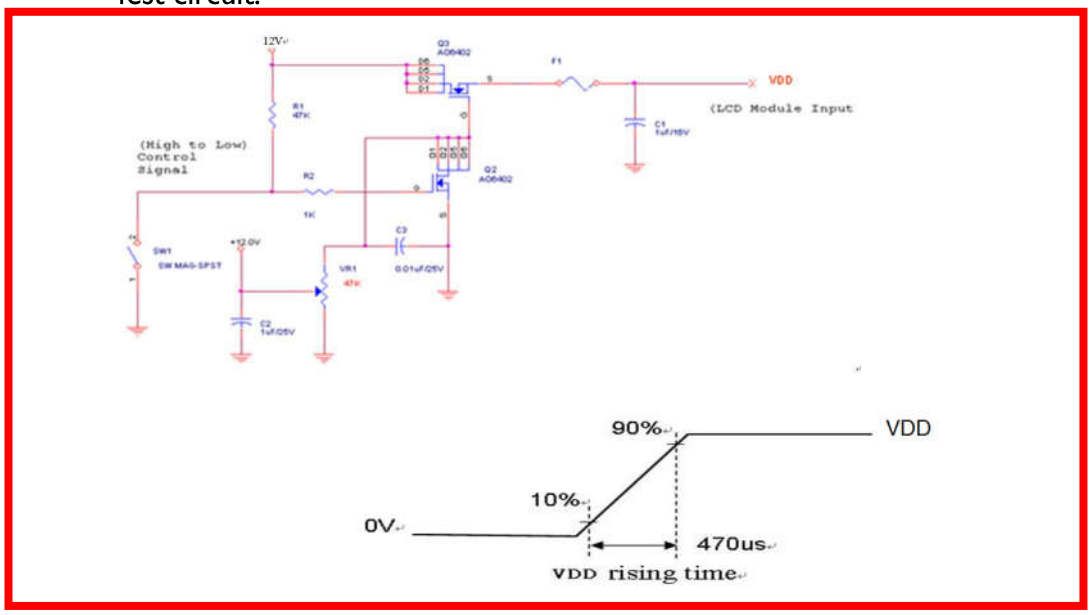
Permanent damage may occur if exceeding the following maximum rating.

Symbol	Description	Min	Max	Unit	Remark
VDD	Power Supply Input Voltage	GND-0.3	14	[Volt]	Ta=25℃
SCL,SDA, WP	I2C input Voltage	GND-0.3	4.0	[Volt]	Ta=25℃

3.3.2 Recommended Operating Condition

Symbol	Description	Min	Typ	Max	Unit	Remark
VDD	Power supply Input voltage	10.8	12	13.2	[Volt]	
IDD	Power supply Input Current (RMS)	-	0.60	0.72	[A]	VDD= 12V, Whie Pattern, Fv=60Hz
			0.68	0.82	[A]	VDD= 12V, Whie Pattern, Fv=76Hz
PDD	VDD Power Consumption	-	7.2	8.64	[Watt]	VDD= 12V, Whie Pattern, Fv=60Hz
			8.16	9.84	[Watt]	VDD= 12V, Whie Pattern, Fv=76Hz
IRush	Inrush Current	-	-	3.0	[A]	***Note 3-1***
VDDrp	Allowable VDD Ripple Voltage	-	-	VDD*5%	[mV]	VDD= 12V, White Pattern, Fv=66Hz

Note 3-1: Inrush Current measurement:
Test circuit:



The duration of VDD rising time: 470us.

3.3.3 Input control signal threshold voltage definition

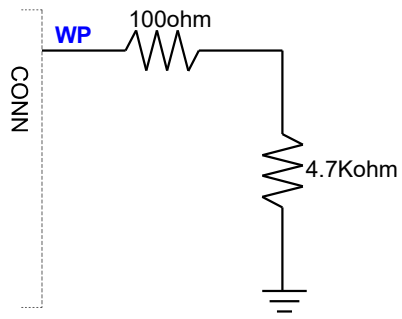
Item	Symbol	Min.	Typ.	Max.	Unit
Input High Threshold Voltage	VIH	2.7	-	3.6	V
Input Low Threshold Voltage	VIL	0	-	0.6	V

3.3.4 Write Protection mode selection

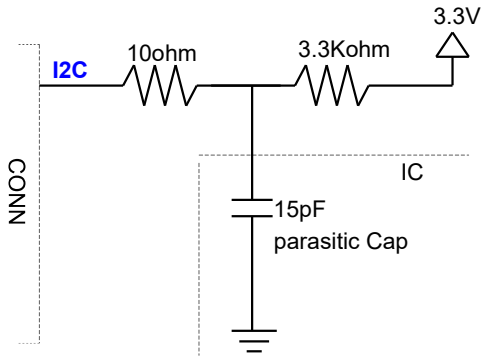
WP	Note
L or OPEN	Protection
H	Writable

3.3.5 Input equivalent impedance

1. Input equivalent impedance of WP pin

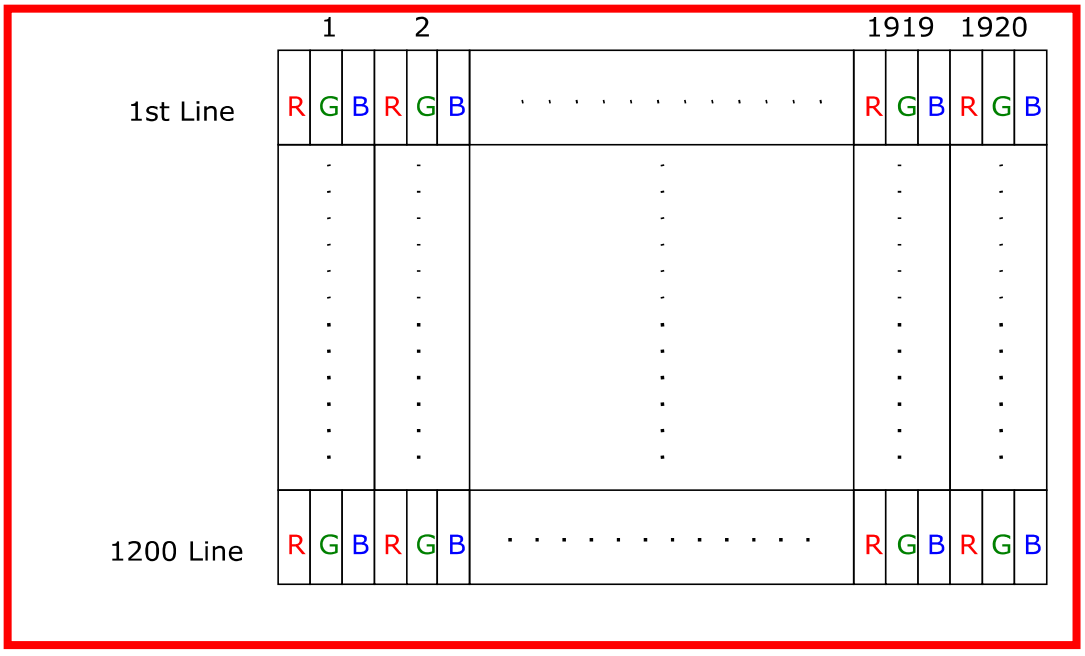


2. Input equivalent impedance of SDA/SCL pin

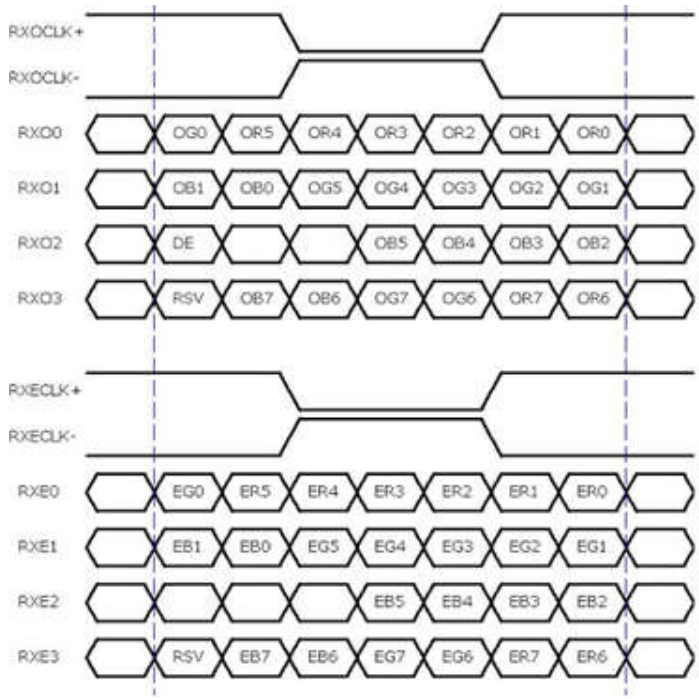


3.4 Signal Characteristics

3.4.1 LCD Pixel Format



3.4.2 LVDS Data Format



8 Bit Color Bit Order			
MSB	R7	G7	B7
	R6	G6	B6
	R5	G5	B5
	R4	G4	B4
	R3	G3	B3
	R2	G2	B2
	R1	G1	B1
LSB	R0	G0	B0

Note 3-2:

a. O = "Odd Pixel Data" E = "Even Pixel Data"

b. Refer to 3.4.1 LCD pixel format, the 1st data is 1 (Odd Pixel Data), the 2nd data is 2 (Even Pixel Data) and the last data is 1920 (Even Pixel Data).

3.4.3 Color versus Input Data

The following table is for color versus input data (8bit). The higher the gray level, the brighter the color.

Color	Gray Level	Color Input Data																								Remark
		RED data (MSB:R7, LSB:R0)								GREEN data (MSB:G7, LSB:G0)								BLUE data (MSB:B7, LSB:B0)								
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0	
Black	-	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
White	-	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
Gray 127	-	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	
Red	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Green	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
Blue	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	

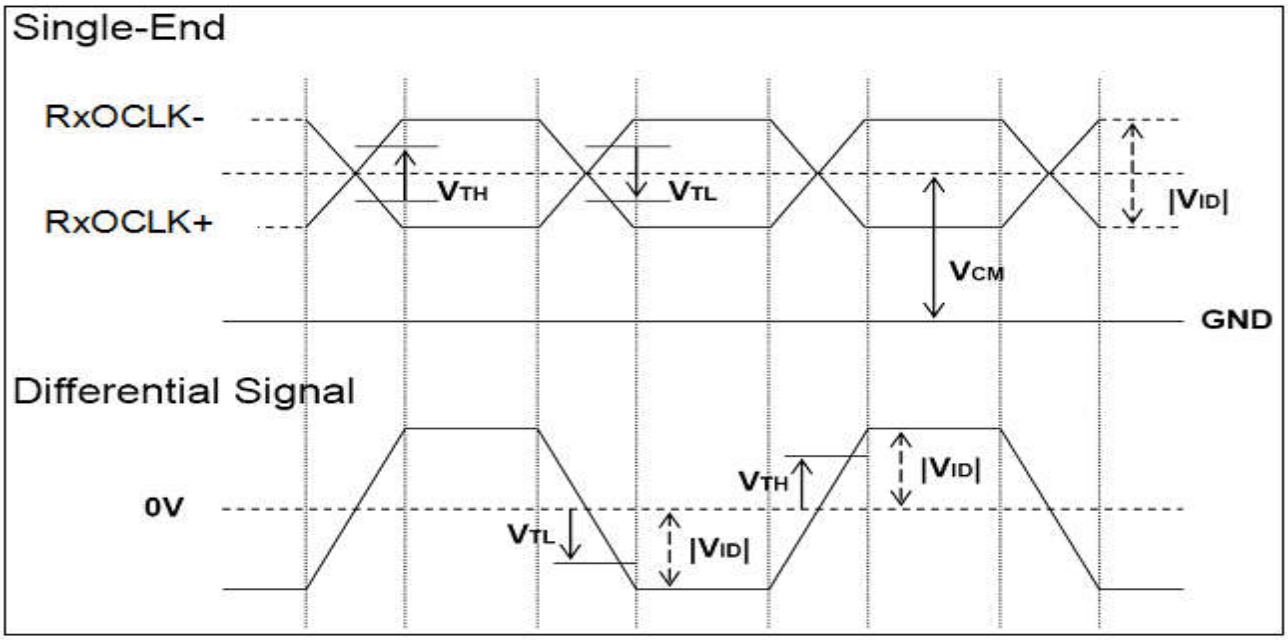
3.4.4 LVDS Specification

a. DC Characteristics:

Symbol	Description	Min	Typ	Max	Units	Condition
V_{TH}	LVDS Differential Input High Threshold	-	-	+100	[mV]	V_{CM} = 1.2V
V_{TL}	LVDS Differential Input Low Threshold	-100	-	-	[mV]	V_{CM} = 1.2V
$\lvert V_{ID} \rvert$	LVDS Differential Input Voltage	100	-	600	[mV]	
V_{CM}	LVDS Common Mode Voltage	+1.0	+1.2	+1.5	[V]	V_{TH}-V_{TL} = 200mV

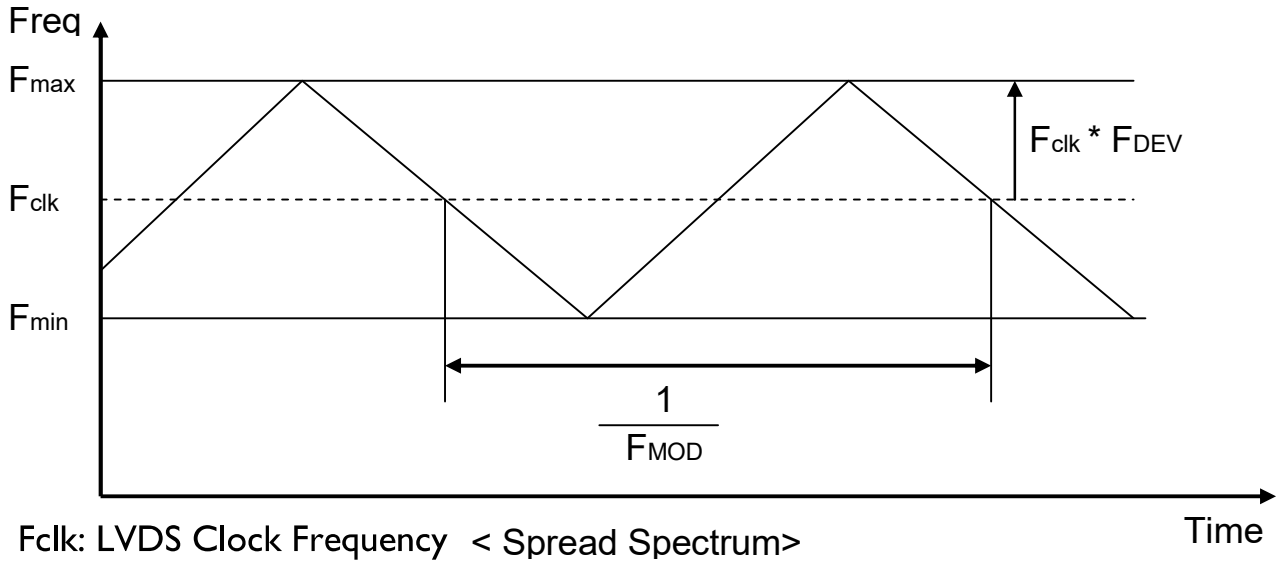
LVDS Signal Waveform:

Use RxOCLK- & RxOCLK+ as example.



b. AC Characteristics:

Symbol	Description	Min	Max	Unit	Remark
F_{DEV}	Maximum deviation of input clock frequency during Spread Spectrum	-	± 3	%	
F_{MOD}	Maximum modulation frequency of input clock during Spread Spectrum	-	200	KHz	



Fclk: LVDS Clock Frequency < Spread Spectrum>

3.4.5 Input Timing Specification

It only support DE mode, and the input timing are shown as the following table.

Symbol	Description		Min.	Typ.	Max.	Unit	Remark
Tv	Vertical Section	Period	1092	1130	1818	Th	
Tdisp (v)		Active	1080	1080	1080	Th	
Tblk (v)		Blanking	12	50	738	Th	
Fv		Frequency	48	60	76	Hz	
Th	Horizontal Section	Period	1034	1050	1100	Tclk	
Tdisp (h)		Active	960	960	960	Tclk	
Tblk (h)		Blanking	74	90	140	Tclk	
Fh		Frequency	52.0	68.0	91.0	KHz	***Note 3-3***
Tclk	LVDS Clock	Period	10.6	14.0	18.5	ns	1/Fclk
Fclk		Frequency	54.2	71.2	94.0	MHz	***Note 3-4***

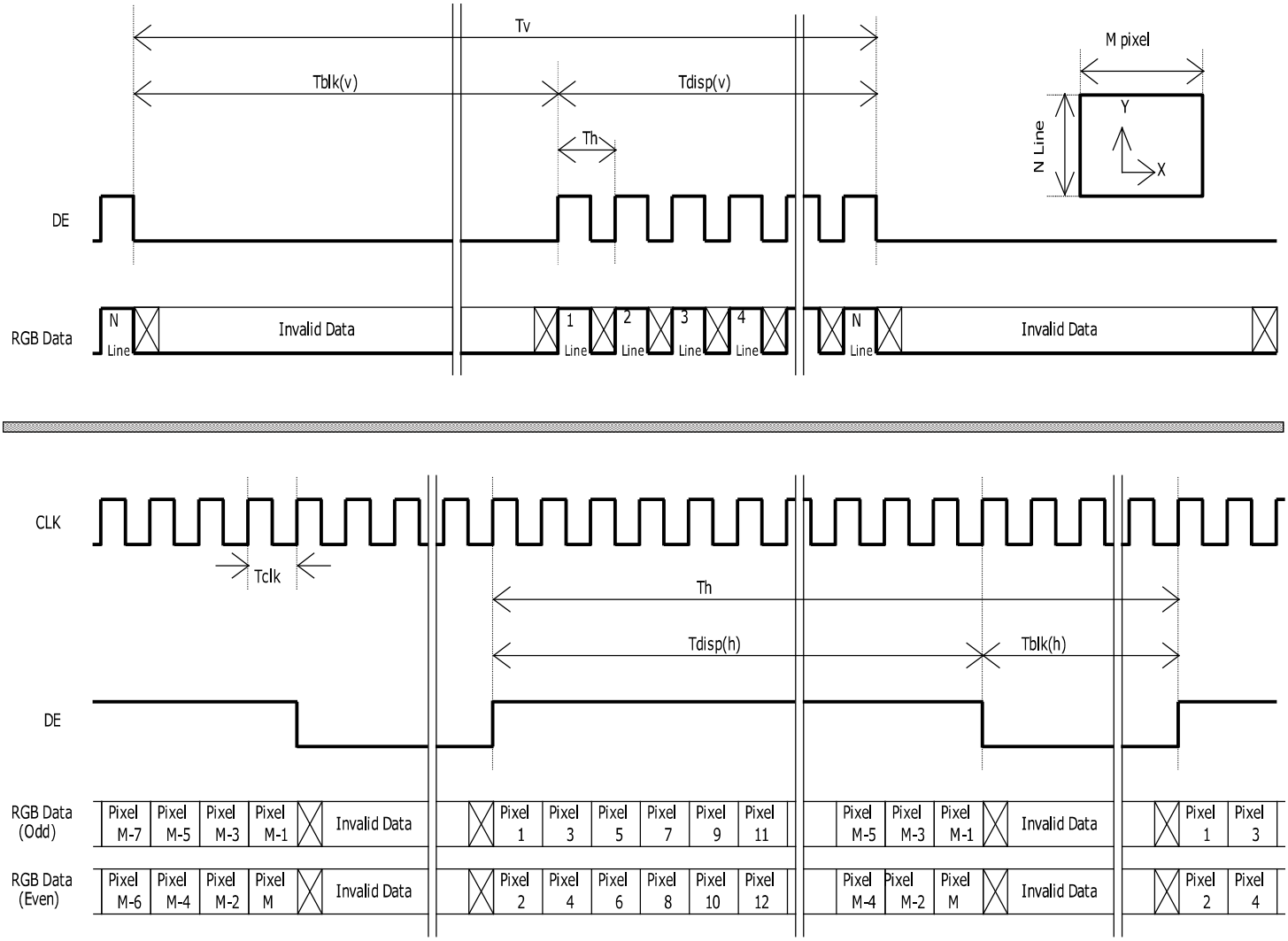
Note 3-3: The equation is listed as following. Please don't exceed the above recommended value.

Fh (Min.) = Fclk (Min.) / Th (Min.);
Fh (Typ.) = Fclk (Typ.) / Th (Typ.);
Fh (Max.)= Fclk (Max.) / Th (Min.);

Note 3-4: The equation is listed as following. Please don't exceed the above recommended value.

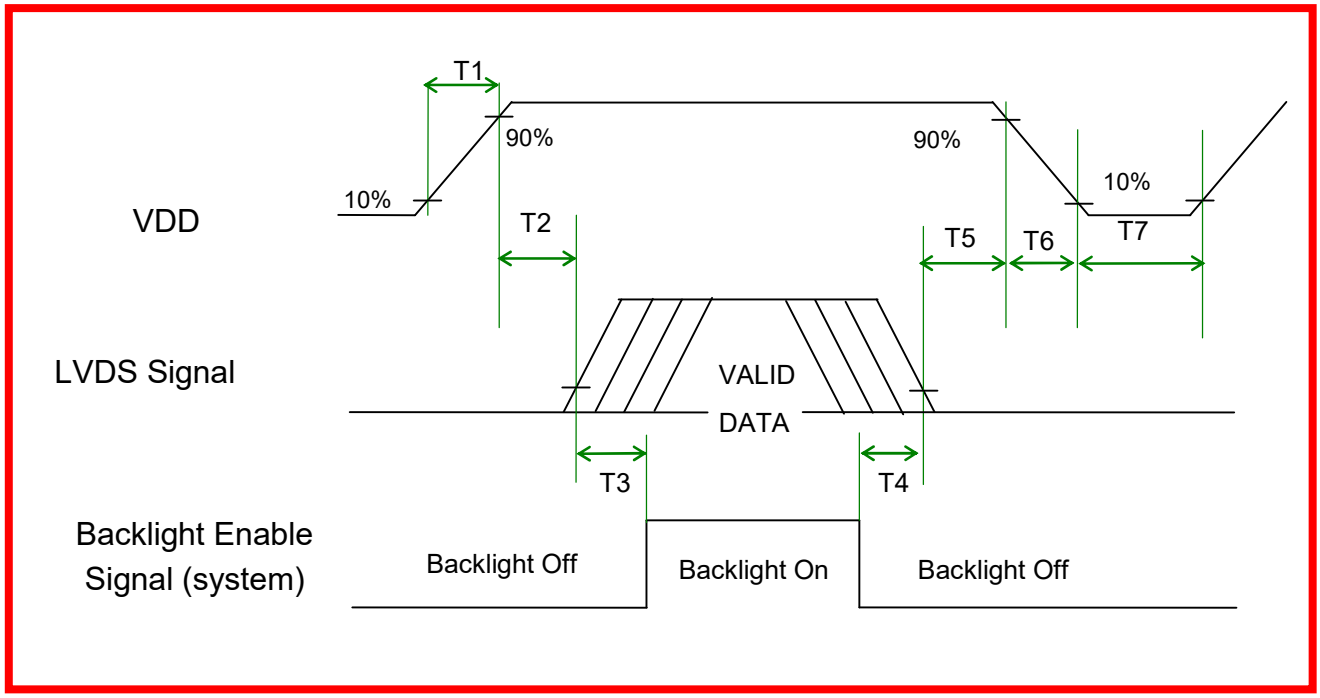
Fclk (Min.) = Fv (Min.) x Th (Min.) x Tv (Min.);
Fclk (Typ.) = Fv (Typ.) x Th (Typ.) x Tv (Typ.);
Fclk (Max.) = Fv (Max.) x Th (Typ.) x Tv (Typ.);

3.4.6 Input Timing Diagram



3.5 Power ON/OFF Sequence

VDD power,LVDS signal and backlight on/off sequence are as following. LVDS signals from any system shall be Hi-Z state when VDD is off.



Power Sequence Timing

Symbol	Value			Unit	Remark
	Min.	Typ.	Max.		
T1	0.5	-	10	[ms]	
T2	0	-	200	[ms]	
T3	500	-	-	[ms]	
T4	100	-	-	[ms]	
T5	0		50	[ms]	*Note 3-5* *Note 3-6*
T6	0	-	200	[ms]	*Note 3-6* *Note 3-7*
T7	1000	-	-	[ms]	

Note 3-5 : Recommend setting T5 = 0ms to avoid electronic noise when VDD is off.

Note 3-6 : During T5 and T6 period , please keep the level of input LVDS signals with Hi-Z state.
Note 3-7 : Voltage of VDD must decay smoothly after power-off. (customer system decide this value)

3.6 Backlight Specifications

Ta = 25 ± 2 °C

Parameter			Symbol	Values			Unit	notes
				Min	Typ	Max		
LED Driver :								
Power Supply Input Voltage			VBL	21.6	24.0	26.4	Vdc	1
Power Supply Input Current			IBL	-	2.50		A	1
Power Supply Input Current (In-Rush)			In-rush	-	-	(TBD)	A	V_{BL} = 24.0V $ExtV_{BR-B}$ = 100% 3
Power Consumption			PBL	-	60		W	1
Input Voltage for Control System Signals	On/Off	On	V on	2.5	-	5.5	Vdc	
		Off	V off	-0.3	0.0	0.5	Vdc	
	Brightness Adjust		$ExtV_{BR-B}$	30	-	100	%	On Duty 5
				30	-	100	%	
	$ExtV_{BR-B}$ Frequency		f_{PWM}	500	-	1500	Hz	
	Pulse Duty Level (PWM)		High Level	2.5	-	5.5	Vdc	HIGH : on duty LOW : off duty
			Low Level	0.0	-	0.5	Vdc	
LED :								
Life Time				30,000	50,000		Hrs	2

notes :

1. Electrical characteristics are determined after the unit has been 'ON' and stable for approximately 60 minutes at 25±2°C. The specified current and power consumption are under the typical supply Input voltage 24Vand V_{BR} ($ExtV_{BR-B}$: 100%), it is total power consumption.
2. The life time (MTTF) is determined as the time which luminance of the LED is 50% compared to that of initial value at the typical LED current (ExtVBR-B :100%) on condition of continuous operating in LCM state at 25±2°C.
3. The duration of rush current is about 200ms. This duration is applied to LED on time.
4. Even though inrush current is over the specified value, there is no problem if I^2T spec of fuse is satisfied. $ExtV_{BR-B}$ signal have to input available duty range and sequence.
5. After Driver ON signal is applied, $ExtV_{BR-B}$ should be sustained from 30% to 100% more than 500ms. After that, $ExtV_{BR-B}$ 30% and 100% is possible

4 Reliability Test

AUO reliability test items are listed as following table. (*Bare Panel only*)

Items	Condition	Remark
Temperature Humidity Bias (THB)	Ta= 50℃, 80%RH, 300hours	
High Temperature Operation (HTO)	Ta= 50℃, 50%RH, 300hours	
Low Temperature Operation (LTO)	Ta= 0℃, 300hours	
High Temperature Storage (HTS)	Ta= 60℃, 300hours	
Low Temperature Storage (LTS)	Ta= -20℃, 300hours	
Thermal Shock Test (TST)	-20℃/30min, 60℃/30min, 100 cycles	***Note 4-1***

Note 4-1: a. A cycle of rapid temperature change consists of varying the temperature from -20℃ to 60℃, and back again. Power is not applied during the test.
b. After finish temperature cycling, the unit is placed in normal room ambient for at least 4 hours before power on.

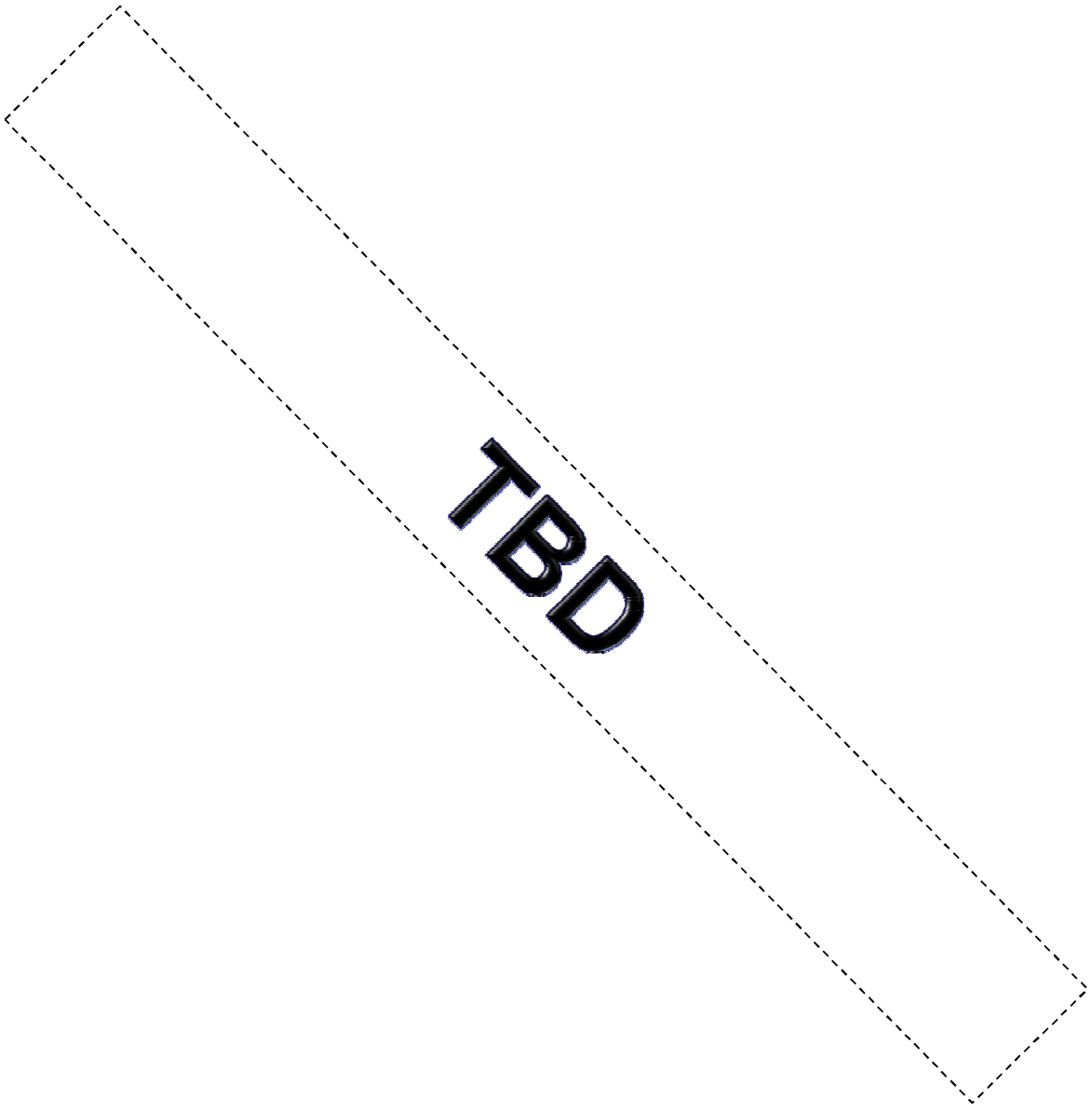
5 Shipping Label

5.1 Small Shipping Label

The label is on the PCBA as shown below :

5.2 Run Card Label

The label is on the panel as shown below :



6.0 MECHANICAL CHARACTERISTICS

6.1 Dimensional Requirements

FIGURE 6 (located in Appendix) shows mechanical outlines for the model YZ270CD01-DE. Other parameters are shown in Table 8.

<Table 8. Dimensional Parameters>

Item	Value	
Outline Dimension	Horizontal	628.0 mm
	Vertical	366.4 mm
	Depth	18.5 mm(Typ.),26.9mm(Max.)
Bezel Area	Horizontal	600.0 mm
	Vertical	338.4 mm
Active Display Area	Horizontal	597.888 mm
	Vertical	336.312 mm
Weight	3.75 Kg(Typ.), 3.85 Kg(Max.)	

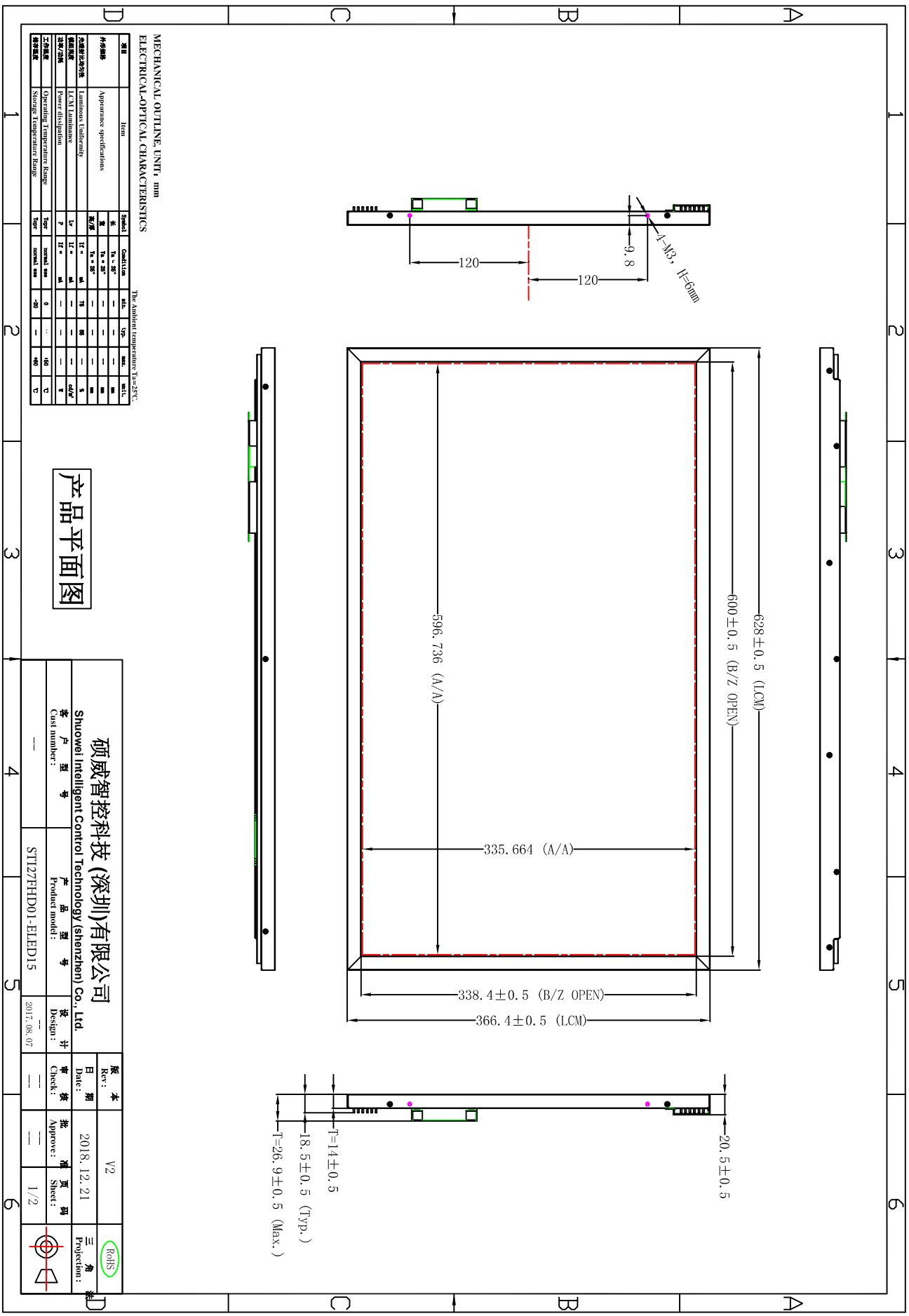
Note : Please refer to a mechanical drawing in terms of tolerance at the next page.

MECHANICAL OUTLINE, UNIT: mm

ELECTRICAL-OPTICAL CHARACTERISTICS

The Ambient temperature Ta=25℃

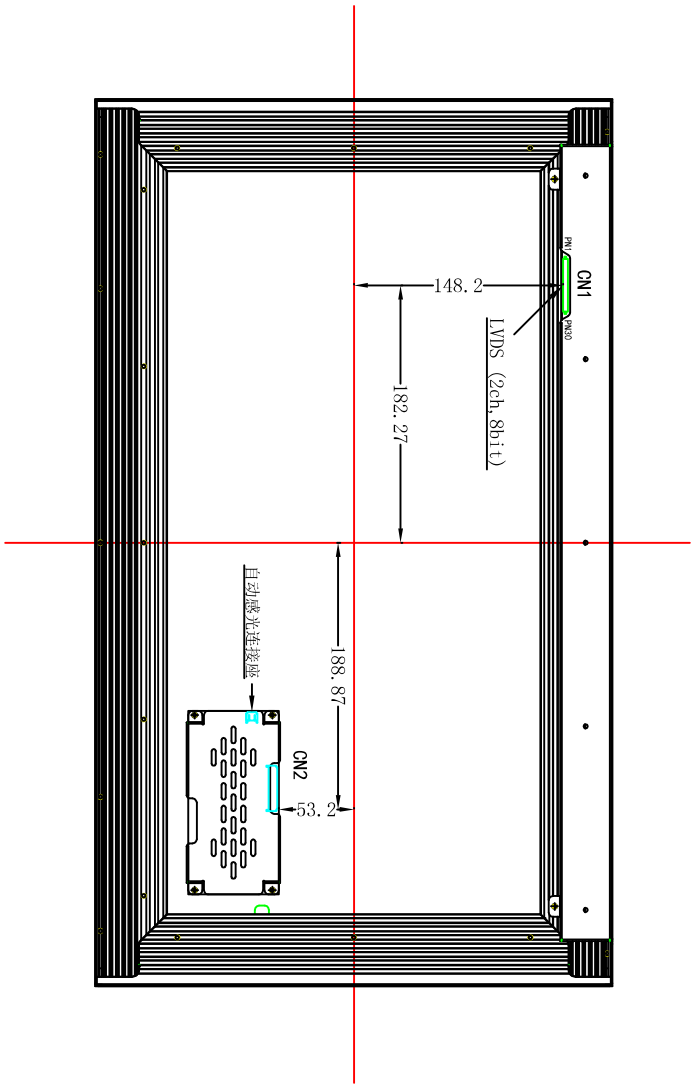
项目	Item	Symbol	Condition	min.	typ.	max.	unit
外形特性	Appearance specifications	长	Ta = 25°	—	—	—	mm
		宽	Ta = 25°	—	—	—	mm
		厚/厚	Ta = 25°	—	—	—	mm
光学特性/均匀性	Luminous Uniformity	Lv	If = mA	75	85	—	%
背光亮度	LCM Luminance	If	If = mA	—	—	—	mA
功率/功耗	Power dissipation	P	If = mA	—	—	—	mW
工作温度	Operating Temperature Range	Topr	normal use	0	...	+50	℃
储存温度	Storage Temperature Range	Topr	normal use	-20	—	+60	℃



产品平面图

硕威智控科技 (深圳)有限公司 Shuowei Intelligent Control Technology (shenzhen) Co., Ltd.			
客户型号 Cust number :	—	设计 Design :	2017.08.07
产品型号 Product model :	STI27FHD01-ELED15	审核 Check :	—
		批准 Approve :	—
		版本 Rev :	V2
		日期 Date :	2018.12.21
		页码 Sheet :	1/2
		三角 Projection :	RoHS

MECHANICAL OUTLINE, UNIT: mm



ELECTRICAL-OPTICAL CHARACTERISTICS

The Ambient temperature Ta=25℃

项目	Item	Symbol	Condition	min.	typ.	max.	unit
外形特性	Appearance specifications	长	Ta = 25°	—	—	—	mm
		宽	Ta = 25°	—	—	—	mm
		厚/厚	Ta = 25°	—	—	—	mm
光学特性与均匀	Luminous Uniformity	Lv	If = mA	75	85	—	%
最高亮度	LCM Luminance	Lv	If = mA	—	—	—	cd/㎡
功率/功耗	Power dissipation	P	If = mA	—	—	—	W
工作温度	Operating Temperature Range	Topr	normal use	0	...	+50	℃
储存温度	Storage Temperature Range	Topr	normal use	-20	—	+60	℃

CN2 (PH2.0X14)

PIN	Symbol	Description	note
1	VCC	Power supply voltage +24V	
2	VCC	Power supply voltage +24V	
3	VCC	Power supply voltage +24V	
4	VCC	Power supply voltage +24V	
5	VCC	Power supply voltage +24V	
6	GND	Power ground	
7	GND	Power ground	
8	GND	Power ground	
9	GND	Power ground	
10	GND	Power ground	
11	NC		
12	ON/OFF	Output enable signal	
13	DIM	Dimming signal	
14	NC		

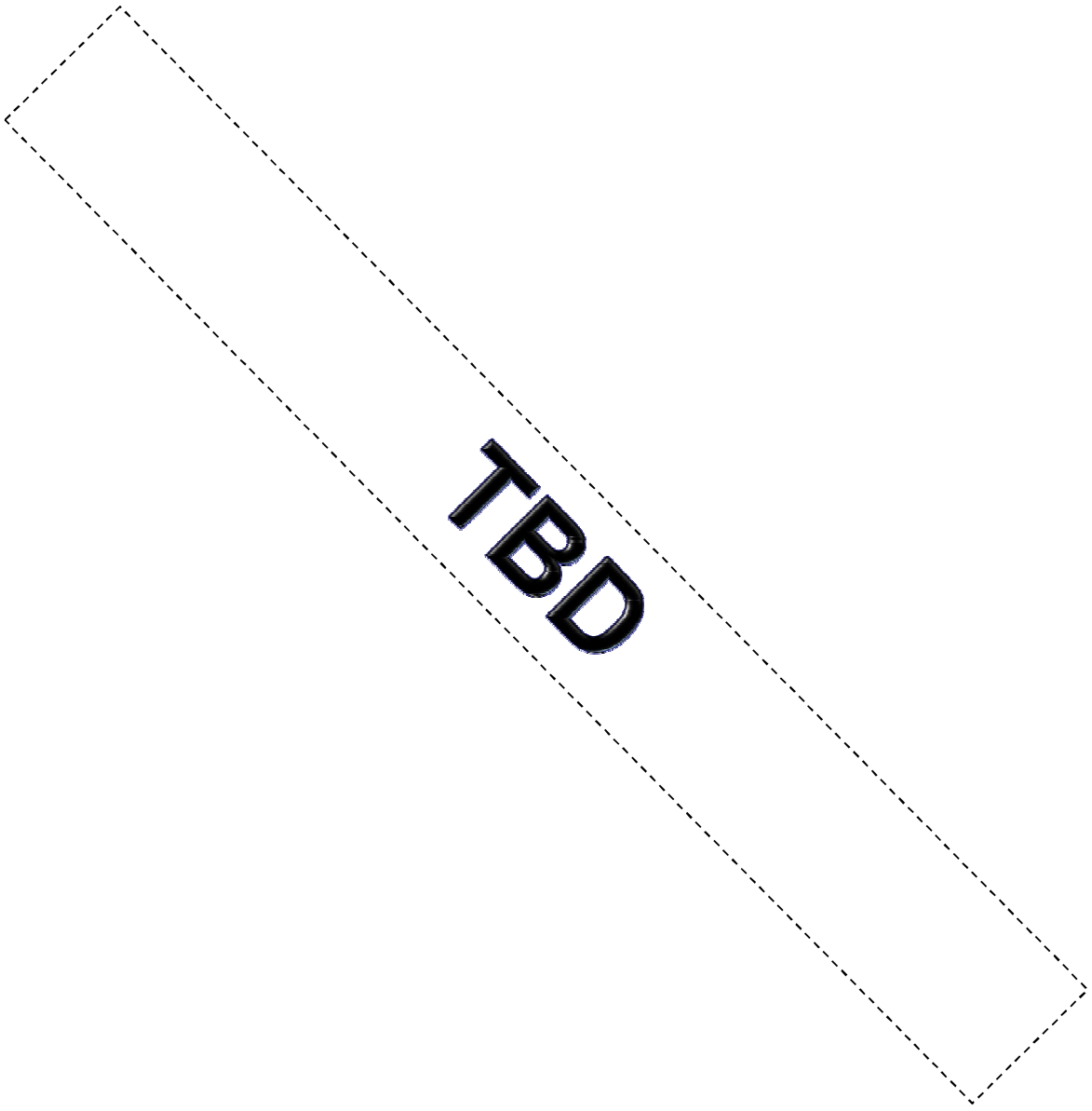
产品平面图

硕威智控科技 (深圳)有限公司
Shuowei Intelligent Control Technology (shenzhen) Co., Ltd.

客户型号 Cust number:	—	产品型号 Product model:	STI27FHD01-ELED15	设计 Design:	2017.08.07	版本 Rev:	V2	RoHS
				审核 Check:	—	日期 Date:	2018.12.21	
				批准 Approve:	—	页码 Sheet:	2/2	
						第一角 Projection:		

7 Packing Specification

7.1 Packing Flow



7.2 Pallet and Shipment information